





<u>McSAFE</u> – High Performance Monte Carlo Methods for SAFEty Demonstration

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Project Goals: move MC-methods towards industrial applications

- Generalize and optimized N/TH/TM coupling
- Optimize depletion simulations (stability, CPU, memory requirements)
- Extension of MC-codes for transient analysis e.g. RIA (Safety)
- Validate MC tools using experimental data
- Full core simulations at pin-level using HPC

McSAFE: MC-Based Multiphysics

- Provide reference solutions for low-order solvers
- ➔ Industry-like applications



OUTLOOK

- Validation using plant data and tests
- Optimization of codes/methods for HPCsimulations
- Optimizations to reduce CPU-usage for full core depletion
- Reduce statistical uncertainties of MC-codes
- Applications to PWR, VVER and SMR



McSAFE: MC/TH Simulations





PWR FA: SERPENT/SCF/TU

HEX FA: SERPENT/SCF/TU

SERPENT/SUBCHANLFOW: Analyis of a REA in Minicoret

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X-Y power distribution at Time: 4.9 s

McSAFE User Group

User Group established

PW

- To join the UG contact: victor.sanchez@kit.edu
- Test the tools and give your feedbacks

Visit our Website: www.mcsafe-h2020.eu