

Key Building Block Optimization for High-Performance Transceivers: Multimode Interferometers from Conventional to Sub-Wavelength Regime

Yunlong Zhang^{*}, Marc Schneider, Djorn Karnick, Lars Eisenblätter, Thomas Kühner, Marc Weber

Karlsruhe Institute of Technology, Institute for Data Processing and Electronics
Hermann-von-Helmholtz-Platz 1, 76344 Eggenstein-Leopoldshafen, Germany
E-mail: zhang.yunlong@partner.kit.edu, djorn.karnick@kit.edu,
marc.schneider@kit.edu, lars.eisenblaetter@kit.edu,
thomas.kuehner@kit.edu, marc.weber@kit.edu

Multimode interferometers (MMIs) are key components for high-bandwidth transceivers in upgrading the data transmission of future detector systems. We present two conventional high-performance MMIs fabricated on a 250 nm SOI platform with different power splitting ratios which operate as 50%:50% power splitter for Mach-Zehnder modulators and as 86%:14% power splitter for loop control in future transceiver designs, respectively. Besides, we present the novel MMIs based on sub-wavelength gratings. By engineering the refractive index of relevant parts, more compact 50%:50% MMIs and multi-port, low phase error MMIs become feasible.

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^{*}Speaker

1. Introduction

The Talbot effect was first observed in 1836 [1]. It implies that a monochromatic optical planar wave hitting upon a periodic diffraction grating, will create repeated grating images at a fixed distance L, away from the diffraction grating. The regular distance L is called the Talbot length. After lots of attempts of analysis and derivation [2]-[6], the theory and understanding of this phenomenon became mature. The fundamental idea of multimode interferometers (MMIs) is to induce a confined Talbot effect [7]-[8], and this concept was tested by Ulrich et al. [9]. Since then, MMIs received significant attention and were widely applied, especially in silicon photonics integration, due to their compactness and broadband characteristics [10] [12].

With the development of fabrication technology, researchers tried to improve the performance of MMI further with sub-wavelength gratings. A sub-wavelength grating is a periodic structure at a scale smaller than the wavelength of the light propagating through it. Thereby diffraction effects are suppressed and the corresponding waveguide can be regarded as a homogeneous waveguide. Literature [13] shows that the performance of MMIs deteriorates dramatically, when they are fabricated on a high-index contrast platform, for instance silicon-on-insulator (SOI). Under these circumstances, utilizing sub-wavelength structures is very attractive [14]-[15].

In this paper, we present the design of two different conventional MMIs with different splitting ratios, rigorous simulation results from COMSOL Multiphysics and measurement results of the fabricated devices. In addition, the sub-wavelength MMIs were studied and the corresponding simulation results are presented as well.

2. Conventional and sub-wavelength MMIs

2.1 MMI fundamentals

The design of conventional MMIs usually starts from the calculation of propagation constants, which are calculated by (1) [10], where k is the free space wavenumber, n is the refractive index of the waveguide core, W is the width of MMI and m is the mode order. The required length of the device is dominantly determined by the beat length L_{π} (2) [16], therefore the first and second order propagation constants are rather important. It is worth mentioning that, for high-quality imaging, the propagation constants are required to satisfy the quadratic relation in (3) [16].

$$(1) \quad \beta_m = kn \sqrt{1 - (\frac{m\pi}{Wkn})^2}$$

(2)
$$L_{\pi}(\lambda) = \frac{\pi}{\beta_1(\lambda) - \beta_2(\lambda)}$$

(3)
$$\beta_m = \beta_1 - (m^2 - 1)\pi/(3L_\pi)$$

Taking the number of ports, their relative positions and the length of the multimode region into account, the number of images and their optical intensity ratio can be designed and selected accordingly. There have already been abundant publications about this analysis and calculation [10] [16] so that this will not be the focus of this paper.

2.2 Two conventional MMIs

3-dB (50%:50% power splitting ratio) MMIs are widely used as power splitter and power combiner. In our design, we set the width of the MMI to 4.5 μ m and swept the length of the MMI to optimize the design via numerical simulation. The optimal length of the multimode region is 24.5 μ m. In the same way, we optimized the geometry of an 86%:14% MMI, resulting in a geometry of 2.8 μ m \times 14 μ m. Fig. 1 and Fig. 2 show numerical simulations of two MMIs at 1550 nm in COMSOL Multiphysics.

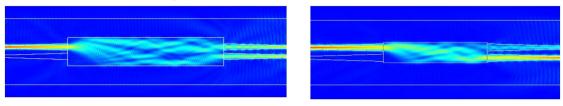


Fig. 1 50%:50% MMI simulation

Fig. 2 86%:14% MMI simulation

We fabricated the above two devices on a 250 nm SOI platform at IMS Chips (Institut für Mikroelektronik Stuttgart, Germany) and characterized them. The measurement and simulation results are illustrated in Fig. 3 a) and b), respectively. It can be seen that theory and experiment are in good agreement with nearly negligible differences. The total insertion loss of both MMIs is below 0.5 dB. Additionally, their splitting ratio remains rather stable throughout the C band.

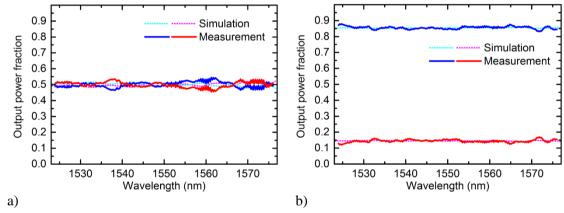


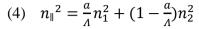
Fig. 3 Output power fraction versus incident wavelength of the a) 50%:50% MMI and b) 86%:14% MMI.

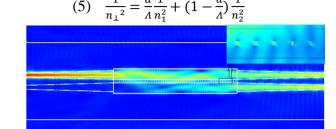
2.3 A sub-wavelength 50%:50% 2×2 MMI

Using the identical width and position of the I/O ports as the device simulated in Fig. 2, we engineered the equivalent refractive index of the middle part of the multimode region, which modifies the profile of even modes. Therefore, the phase difference of the first two order modes will be altered accordingly. The simulation shows required length of 50%:50% MMIs is 14 µm.

One effective way of engineering the refractive index is using sub-wavelength gratings. To start, Rytov's formulae (4) and (5) [14] are important where n_l and n_{\perp} are the directions parallel and perpendicular to the interfaces between the alternating layers, respectively. As Fig. 4 shows, a is the length of the grating and Λ is the pitch of the periodic structure. Optimized parameters are $L_{MMI} = 14 \mu m$ (length of the MMI), a = 60 nm and $\Lambda = 250 \text{ nm}$. The final structure can be seen in Fig. 5 where a row of holes runs horizontally through the center of the MMI,

representing the sub-wavelength grating, where the silicon layer is etched down to the substrate. An inset picture zoomed into the sub-wavelength gratings is inserted to Fig. 5 as well to help understand the structure. The simulated insertion loss of this device within the C band is lower than 0.3 dB and thus marginally better than the value of the conventional 50%:50% MMI presented in Fig. 1, while the on-chip footprint is 38% smaller.





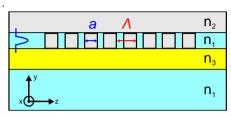


Fig. 4 Sub-wavelength structures from Fig. 5 Simulation of 3-dB sub-wavelength MMI the side view

2.4 A sub-wavelength 4×4 MMI

Based on the previous investigations, we extended the design to multi-port MMIs and present a 4×4 MMI in this section. According to [13], the modal phase error increases with the number of modes and strongly depends on the refractive index contrast. To improve the performance of n×n MMIs, we engineered the refractive index of the side region next to the multimode region to lower the refractive index contrast. Fig. 6 shows the simulation of the proposed 4×4 MMI in COMSOL Multiphysics. The footprint of this sub-wavelength MMI is 7.2 μ m × 106 μ m, slightly larger than the conventional MMI (7.2 μ m × 96 μ m), and its side region has an equivalent refractive index of 2.64 at 1550 nm. Both the conventional and the sub-wavelength 4×4 MMI have an insertion loss around 0.4 dB at 1550 nm, whereas the maximum phase error decreases significantly from 10.3° to 3.1° for the sub-wavelength MMI.

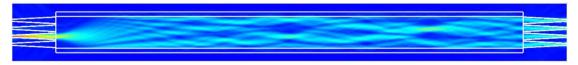


Fig. 6 Simulation of a 4×4 MMI with side regions of engineered refractive index

3. Conclusion

A basic design process of $n\times n$ MMIs is presented in this paper. Two conventional MMIs with different power splitting ratios were simulated and fabricated on a 250 nm SOI platform: 50%:50% and the 86%:14%, respectively. Our simulations are validated through their excellent agreement with the measurement results. Due to their compact structure and low losses, they will be key components for the modulators and loop control in our integrated transceivers designs. By engineering the refractive index of the relevant regions, the 50%:50% 2×2 MMI can be made even smaller compared with conventional ones, and the modal phase error of 4×4 MMIs can be decreased dramatically by a factor of 3. The study in this paper will contribute to

the optimization of our high-bandwidth transmitter [12] design to upgrade detector readout systems.

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