

On Forging SPHINCS⁺-Haraka Signatures on a Fault-tolerant Quantum Computer

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Abstract. SPHINCS⁺ is a state-of-the-art hash based signature scheme, the security of which is either based on SHA-256, SHAKE-256 or on the Haraka hash function. In this work, we perform an in-depth analysis of how the hash functions are embedded into SPHINCS⁺ and how the quantum pre-image resistance impacts the security of the signature scheme. Subsequently, we evaluate the cost of implementing Grover’s quantum search algorithm to find a pre-image that admits a universal forgery.

In particular, we provide quantum implementations of the Haraka and SHAKE-256 hash functions in Q# and consider the efficiency of attacks in the context of fault-tolerant quantum computers. We restrict our findings to SPHINCS⁺-128 due to the limited security margin of Haraka. Nevertheless, we present an attack that performs better, to the best of our knowledge, than previously published attacks.

We can forge a SPHINCS⁺-128-Haraka signature in about $1.5 \cdot 2^{90}$ surface code cycles and $2.03 \cdot 10^6$ physical qubits, translating to about $1.55 \cdot 2^{101}$ logical-qubit-cycles. For SHAKE-256, the same attack requires $8.65 \cdot 10^6$ qubits and $1.6 \cdot 2^{84}$ cycles resulting in about $2.65 \cdot 2^{99}$ logical-qubit-cycles.

Keywords: Post-quantum cryptography · quantum implementation · resource estimation · cryptanalysis.

1 Introduction

Overview and Related Work Ongoing research in the area of quantum technologies has led to the belief that quantum computers will be able to break current public-key cryptosystems within the coming decades. On the contrary, symmetric-key primitives are believed to be somewhat resistant against quantum attacks, with the most promising generic attack being Grover’s search algorithm [15]; Its quadratic improvement over a classical brute force search can easily be countered by doubling the key length of the underlying primitives.

In order to prepare for the (public-key) quantum menace the National Institute for Standards and Technology (NIST) started the post-quantum standardization competition in 2017. From the initial 69 submissions, only 7 were selected as finalists [24]. Additionally, 8 schemes were chosen as alternate candidates based on a high confidence of their security, but with a drawback in

performance compared to the 7 finalists. Briefly speaking, they may be considered as *Backup* candidates for standardization. Among the alternate candidates is the stateless hash-based signature scheme SPHINCS⁺ [16]. SPHINCS⁺ builds on the hardness of inverting one-way functions, i.e., Haraka [20], SHAKE-256 [22] or SHA-256 [11], the first of which can be derived from block-ciphers and thus is believed to provide similar security guarantees against quantum adversaries.

An estimate of the security of SPHINCS⁺, based on cryptographic assumptions, was given within the scope of the NIST submission: The authors considered general attacks [16, Sec. 9.3.1] on the distinct-function multi-target second preimage resistance of the underlying hash functions and estimated the success probability of such an attack as $\Theta((q_{\text{hash}}+1)^2/2^n)$, where q_{hash} is the number of hash queries and n a security parameter. Generally, they quantify the security based on the number of required hash function invocations and thus on the probability of an successful adversary.

The NIST competition features 5 security levels [24,23]: The first level provides security equivalent to performing a key search on AES-128, the second a collision attack on SHA-256 and the fifth a key search on AES-256. Moreover they categorize attacks with quantum computers according to the maximal circuit depth, where each level resembles a number of gates that can be serially computed over a plausible time period. Specifically, NIST estimates that quantum circuits up to a depth of 2^{40} gates can be computed within a single year, up to a depth of 2^{64} in a single decade and up to 2^{96} in a millennium. Respectively, the number of quantum gates to break AES is estimated by NIST to be $2^{170}/\text{MAXDEPTH}$, i.e. 2^{130} , 2^{104} and 2^{74} . [17] gave precise estimates for attacking AES-128 for different values of maxdepth and respective parallelization. Equivalently, NIST estimates 2^{143} classical computational steps. However, we note the most promising attack on AES-128 can be performed in $2^{126.1}$ classical steps as shown by [4]. SPHINCS⁺ features parameters for each security levels, i.e. SPHINCS⁺-SHAKE-256 and SPHINCS⁺-SHA-256 both provide a sufficient amount of security for all 5 NIST security levels. On the other side, SPHINCS⁺-Haraka achieves security level 1 or 2 at most.

An analysis of the security of SHAKE-256 has been given by [1], whose result is the main motivation for our work. They present a quantum circuit to implement a Grover search and attack the 256-bit pre-image resistance of the SHA3-256 hash function and give concise and fault-tolerant estimates for the resources required to implement such a circuit: They claim that their circuit requires $2^{153.8}$ surface code cycles using $2^{12.6}$ logical qubits, resulting in an overall requirement of about $2^{166.4}$ logical-qubit-cycles using 2^{128} black box queries for a 256-bit preimage search. Their results may be adapted to estimate the work required to break the hash function for the SPHINCS⁺ signature scheme. However, there is still considerable ambiguity on the specific construction to forge a signature.

The quantum security of Haraka has not been explicitly analyzed yet. However, due to the capacity of the sponge construction in SPHINCS⁺-Haraka using only 256 bits, attacking the second-preimage-resistance as described in [3] only

requires about $2^{129.5}$ classical hash function invocations, producing a collision on the internal state of the hash function in the process. The best known generic quantum collision attacks on hash functions is the BTH algorithm by [7], which finds a collision using $\mathcal{O}(2^{n/3})$ Grover iterations, (where n is a security parameter), however, also requiring $\mathcal{O}(2^{n/3})$ quantum RAM (QRAM). The concept of QRAM is highly controversial, as quantum states that interact with the environment eventually decay. [10, Thm 2] presented a trade-off using only $\tilde{\mathcal{O}}(n)$ QRAM but $\tilde{\mathcal{O}}(2^{2n/5})$ Grover iterations, resulting in a work effort of about 2^{102} iterations for a collision search with $n = 256$ on Haraka.

The (quantum) invocation of the hash function induces a significant overhead and has to be accounted for. Moreover the implementation on a fault-tolerant quantum computer requires additional overhead to compensate for error correction within the circuit. In our analysis we adapt the concept of logical-qubit-cycles as quantum cost metric, such that each cycle is roughly equivalent to a single (classical) hash function invocation [1]. Briefly speaking, a logical-qubit-cycle is the time-space product of the (fault-tolerant) number of quantum gates and the number of qubits (space) that is used during the computation. The cost to implement the generic attack on a fault-tolerant quantum computer has not been analyzed yet. Instead we can consider the time-space product of Grover iterations and memory, which is $\tilde{\mathcal{O}}(2^{3n/5})$, resulting in a cost of about 2^{153} .

Contribution In this work we consider attacks on SPHINCS⁺ based on inverting the underlying hash functions at specific points, i.e. attacking the XMSS or FORS structure. We chose particularly Haraka, because of its placement as a potential component within the NIST competition. Moreover, preimage resistance of the Haraka [20] hash function has not, to the best of our knowledge, been explicitly evaluated in the quantum setting in any literature. We evaluate the logical resources required to implement our attacks on the Haraka as well as the SHAKE-256 hash-functions and further estimate the fault-tolerant cost to attack the SPHINCS⁺-128 scheme. For the sake of completeness and comparability we also present the numbers to attack the SPHINCS⁺-256 scheme.

In Section 2 we recall parts of the SPHINCS⁺, Haraka and SHAKE-256 scheme, and review the Grover algorithm with respective metrics for fault-tolerant quantum computing. In our work, we use the logical-qubit-cycles metric (introduced in [1]) which compares to classical hash function invocations. Section 3 shows the results for our implementation¹ of the hash functions in Q#. To construct a circuit for Haraka, we partially reused the work of [17] on AES functions, resulting in the first implementation of the Haraka hash function in the quantum setting. The implementation for SHAKE-256 was built from scratch. For both circuits, we consider the number of qubits as well as different metrics based on the gate count and T-Depth. As a result, our implementation of the Haraka512 permutation in the hash function consumes about $2.2 \cdot 10^6$ quantum gates on 1144 logical qubits. Our Keccak permutation in the SHAKE-256 hash function consumes about $3.3 \cdot 10^6$ quantum gates on 3200 logical qubits.

¹ <https://github.com/RobinBerger/Grover-Sphincs>

In Section 4, we analyze the most promising points of attack in the SPHINCS⁺ signature scheme. We propose that the weakest link is the XMSS authentication path for a given WOTS⁺ public key, as this allows a universal forgery attack. Our most promising attack on SPHINCS⁺-128-Haraka requires about $1.6 \cdot 2^{86}$ quantum gates. The same circuit to attack SPHINCS⁺-128-SHAKE-256 has about $1.2 \cdot 2^{86}$ gates.

In Section 5, we partially follow the approach of [1] to estimate the resources for this attack in the context of fault-tolerant quantum computing. We compute the amount of error correction in terms of surface code cycles and the optimal scheme for magic state distillation.

For the Haraka hash function, our attack requires $3.91 \cdot 10^{30} \approx 1.55 \cdot 2^{101}$ logical-qubit-cycles on $2.03 \cdot 10^6$ physical qubits, which is better than the generic quantum collision attack on the hash function, which requires 2^{102} quantum hash function invocations (without considering the cost of implementing the hash function), or a time-space product of 2^{153} , which appears to be the more realistic comparison to the cost of logical-qubit-cycles. Performing our attack with the SHAKE-256 hash function instead requires $7.44 \cdot 10^{29} \approx 2.65 \cdot 2^{99}$ logical-qubit-cycles on $8.65 \cdot 10^6$ physical qubits.

2 Preliminaries

2.1 The SPHINCS⁺ Signature Scheme

In this section we partially review the SPHINCS⁺ signature scheme as proposed and submitted by [16] to the second and third round of NIST’s post-quantum cryptography competition. The structure of the SPHINCS⁺-scheme combines a hypertree (HT) of eXtended Merkle Signature Schemes (XMSS) and Winternitz One-Time Signature schemes (WOTS) with a Forest Of Random Subsets (FORS) as represented in the attack Fig. 3.

In the following, we consider a signature σ_y^x using the scheme y to sign the message x and a hash function $\mathcal{H} \in \{\text{SHAKE-256, Haraka-512, Haraka-sponge}\}$ for all the subsequent hashes. Moreover, each scheme is associated with a $KeyGen(\cdot)$, $Sign(\cdot)$ and $Verify(\cdot)$ functionality. Let $pk_{\text{SPHINCS}^+}, sk_{\text{SPHINCS}^+}$ be a SPHINCS⁺ key pair associated with seeds to deterministically generate the subsequent keys of the scheme. Then a signature of a message m is a tuple of value σ_r and signatures from the hypertree and FORS: $\sigma_{\text{SPHINCS}^+}^m := (\sigma_r || \sigma_{\text{FORS}}^m || \sigma_{\text{HT}}^{pk_{\text{FORS}}})$. The value σ_r will be mostly ignored in the remaining paper.

During signing, one generates a FORS instance, signs a message digest with the FORS key, and signs the FORS pk with the hypertree. The hypertree consists of several layers of XMSS instances. Each XMSS instance is a binary hash tree with WOTS schemes at the leaves, where the value of each node is the output of hashing its child nodes. Each XMSS tree is associated with a root node pk_{XMSS} and a set of WOTS keys $pk_{\text{WOTS}}, sk_{\text{WOTS}}$. An XMSS signature consists of a WOTS signature and an authentication path $\sigma_{\text{XMSS}}^x := (\sigma_{\text{WOTS}}^x, \text{path}_{\text{XMSS}})$,

Algorithm 1: SPHINCS⁺ – KeyGen()

```

1  $sk_{\text{seed}} \xleftarrow{\$} \{0, 1\}^n, sk_{\text{prf}} \xleftarrow{\$} \{0, 1\}^n, pk_{\text{seed}} \xleftarrow{\$} \{0, 1\}^n$ 
2  $pk_{\text{root}} \leftarrow \text{KeyGen}_{\text{HT}}(sk_{\text{seed}}, pk_{\text{seed}})$ 
3 return  $(pk_{\text{SPHINCS}^+} := (pk_{\text{seed}}, pk_{\text{root}}), sk_{\text{SPHINCS}^+} := (sk_{\text{seed}}, sk_{\text{prf}}))$ 
    
```

Algorithm 2: SPHINCS⁺ – Sign($m := \{0, 1\}^*$, sk_{SPHINCS^+})

```

1  $r \xleftarrow{\$} \{0, 1\}^n$ 
2  $\sigma_r \leftarrow \text{prf\_msg}(sk_{\text{prf}}, r, m)$ 
3  $md \leftarrow \mathcal{H}(\sigma_r, pk_{\text{seed}}, pk_{\text{root}}, m)$ 
4  $\sigma_{\text{FORS}}^{md} \leftarrow \text{Sign}_{\text{FORS}}(md, sk_{\text{seed}}, pk_{\text{seed}})$ 
5  $pk_{\text{FORS}} \leftarrow \text{pkFromSig}_{\text{FORS}}(\sigma_{\text{FORS}}, m, pk_{\text{seed}})$ 
6  $\sigma_{\text{HT}}^{\text{pk}_{\text{FORS}}} \leftarrow \text{Sign}_{\text{HT}}(pk_{\text{FORS}}, sk_{\text{seed}}, pk_{\text{seed}}, md)$ 
7 return  $(\sigma := (\sigma_r || \sigma_{\text{FORS}}^{md} || \sigma_{\text{HT}}^{\text{pk}_{\text{FORS}}}))$ 
    
```

where $\text{path}_{X_{MSS}}$ consists of all sibling nodes on the path from a leaf to the root of the tree. The WOTS instances at the leaf nodes are then used to sign the root node of the next layer, resulting in a hypertree. The root node of the top tree is the public key of the hypertree. The bottom WOTS instances represent the respective secret key of the hypertree that is used to create the signature $\sigma_{\text{HT}}^{\text{pk}_{\text{FORS}}}$.

To validate a signature $\sigma_{\text{SPHINCS}^+}^m$, one first computes a FORS public key from σ_{FORS}^m and then verifies the hypertree signature $\sigma_{\text{HT}}^{\text{pk}_{\text{FORS}}}$. For the latter, one has to compute the authentication path through the hypertree and finally compare the resulting public key pk'_{HT} to the key associated with the SPHINCS⁺ signature scheme. The Algorithms 1, 2, 3 review these procedures using the respective signature schemes and a function `prf_msg`, that generates a pseudo-random value as part of the signature. We note that the description is not complete (as in [16]), i.e. it is restricted to a level appropriate to follow the remaining paper.

2.2 Quantum Computing

We assume the reader to be familiar with the basics of quantum information theory (e.g. see [25]). In the following we first describe the general attack strategy using Grover’s algorithm [15]. Then, we recall the setup to estimate quantum resources on a fault-tolerant quantum computing architecture based on the excellent description of [19] using surface codes [13] and magic state distillation [8].

2.3 Grover’s Algorithm on Preimage Resistance

For a fixed n , given a predicate $p : \{0, 1\}^n \rightarrow \{0, 1\}$ marking M elements $x \in \{0, 1\}^n$, Grover’s algorithm finds an element x , for which $p(x) = 1$. Let the initial

Algorithm 3: SPHINCS⁺ – Verify
 $(\sigma := (\sigma_r || \sigma_{FORs}^{md} || \sigma_{HT}^{pk_{FORs}}), m, pk_{SPHINCS^+})$

- 1 $md \leftarrow \mathcal{H}(\sigma_r, pk_{seed}, pk_{root}, m)$
- 2 $pk_{FORs} \leftarrow pkFromSig_{FORs}(\sigma_{FORs}, m, pk_{seed})$
- 3 **return** $Verify_{HT}(pk_{FORs}, \sigma_{HT}, pk_{seed}, md, pk_{root})$

superposition be $|\phi\rangle = \sqrt{(N-M)/N} |\{x|p(x) = 0\}\rangle + \sqrt{M/N} |\{x|p(x) = 1\}\rangle$. Then the algorithm of Grover operates in the space spanned by $|\phi\rangle$ and $|\{x|p(x) = 1\}\rangle$, where $\langle\phi|\{x|p(x) = 1\}\rangle = \sin(\theta)$. The initial value is $\theta = \arcsin(\sqrt{M/N})$, and is increased in every iteration by roughly $\sqrt{M/N}$, where the advance diminishes during the last few iterations. Thus the probability to measure a marked element is the largest after $R = \lceil \pi/4\sqrt{N/M} \rceil$ Grover iterations. Our implementation of the Grover iteration follows the principle construction for oracle invocations.

If the number of matches M is not (exactly) known, and one performs too many iterations, the value of θ decreases. Instead one can run Grover’s algorithm multiple times with different values for M . [6, Theorem 3] have shown that the expected number of iterations remains in $\mathcal{O}(\sqrt{N/M})$.

In the context of hash functions and the random oracle model, we assume the number of matches to be $M = 1$, i.e. we are given a value y and we are looking for a single value x , so that $y = \mathcal{H}(x)$. Whereas there is no guarantee that there are no collisions (i.e. $M > 1$), $M = 1$ is to be expected, since the input and output domain of the hash functions are of equal size in our case.

2.4 Fault-tolerant Resource Estimation

The layered architecture in [19] describes the physical design of a fault-tolerant quantum computer. The first and second layer cover the physical processes and the virtual interfaces of the hardware and are not considered in the analysis. The third layer provides reliable QubitClifford-gates, but not T-gates, by performing a series of measurements and faulty gate applications on physical qubits to correct errors. Each of these intervals is called a surface code cycle. Then, the logical layer provides a universal gate set. The final layer consists of the application of Grover’s algorithm.

In the following we describe the layers in more detail, review the cost metrics of [1] in our setting and explicitly mention the assumptions (since quantum benchmarks are not available) required for the analysis. Our description of the different layers, which are pictured in Fig. 1, is tailored to our resource estimate. We combine these with the cost metrics used by [1] for comparability.

Assumption 1 The cost for a computation of a large-scale fault-tolerant quantum computer is well approximated using surface codes [1,26,18].

The following parameters approximate today’s state of the art [1,12]. We use these for comparability, but note that other values have also been suggested [19].

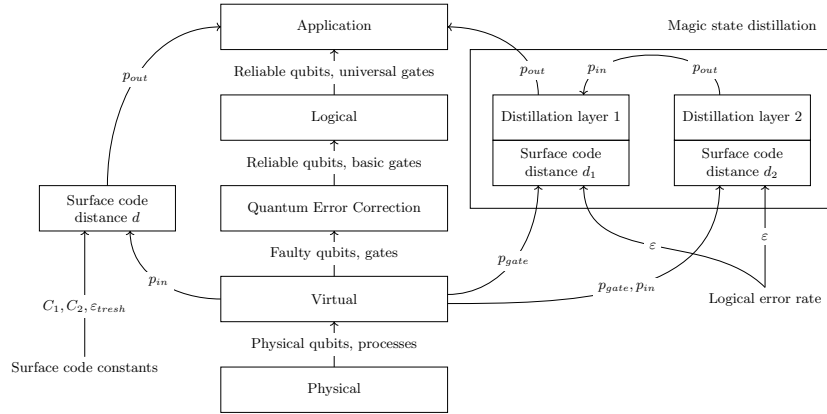


Fig. 1: Layered architecture for quantum computers including parameters for the error correction layer (left) and exemplary magic state distillation (right).

While Assumption 3 does not hold for our oracle implementation per se, it does so for the Grover algorithm over multiple Grover iterations.

Assumption 2 p_{in} is the initial error probability of a quantum state, i.e. before any layer of error correction $p_{in} \approx 10^{-4}$. $p_{gate} \approx p_{in}/10$ is the gate error rate. $T_{sc} = 200ns$ is the approximate time for a single surface code cycle.

Assumption 3 All quantum gates are distributed uniformly across all layers.

Quantum Error Correction Let $C_1, C_2, \epsilon_{trersh}$ be parameters determined by the implementation of the surface code with distance d . Given an initial error rate of p_{in} one can calculate the distance d for a targeted error rate p_{out} as per $p_{out} \approx C_1 (C_2 p_{in} / \epsilon_{trersh})^{\lfloor d+1/2 \rfloor}$ [19, Sec. IV.B]. We follow the suggestion in [14, Fig. 8] and estimate that each logical qubit requires $2 \cdot (d+1)^2$ physical qubits to be implemented in a surface code with distance d .

Logical Layer We deploy the Reed-Muller-15-to-1 distillation introduced by [8], each layer uses 15 magic states with an input error rate of p_{in} and produces one magic state with lower error rate $p_{dist} \approx 35p_{in}^3$. We follow the work of [1] and assume that the amount of logical errors introduced during distillation is already covered in the process resulting in $p_{out} = (1 + \epsilon) p_{dist}$, hence $p_{in} \approx \sqrt[3]{p_{out}/35(1+\epsilon)}$. The distillation is repeated until p_{out} reaches a target value.

Let d_i be a surface code distance for layer i with $i = 1$ being the top layer of distillation, where each distillation requires $10 \cdot d_i$ cycles. For this, [12, Sec. II] gives an example calculation, [1, Alg. 4] gives an explicit algorithm that takes an initial gate error p_{gate} and calculates the number of layers of magic state distillation as well as their respective surface code distance. Each layer i requires $16 \cdot 15^{(i-1)}$ logical qubits. The number of physical qubits in the code is calculated based on the respective surface code.

Algorithm 4: Haraka512Permutation($A[x] : \{0, 1\}^{128}, 0 \leq x < 4$)

```

1 for  $0 \leq i < 5$  do
2   for  $0 \leq j < 4$  do
3      $A[j] := \text{aesEnc}(\text{aesEnc}(A[j], \text{key}_{i,j,1}), \text{key}_{i,j,0})$ 
4      $A := \text{mix}(A)$ 
5 return  $A$ 

```

Application Layer For our implemented circuits we consider the total count for T-, CNOT- and QubitClifford gates, along with the T-depth and T-width, motivated in [17,1]. For a circuit implementing our attacks, i.e. using Grover’s algorithm, let G^d be the total depth (i.e. number of layers) of a circuit and let SCC be the number of surface code cycles for each layer. First, we consider the total number of surface code cycles as $\text{COST}_{\text{SCC}} = \text{SCC} \cdot G^d$. Then, we consider the number of logical qubits Q_G^{log} required to implement the Grover algorithm and the number of logical qubits Q_{MD}^{log} to perform the magic state distillation. Finally, we consider the metric of logical-qubit-cycles from [1, As. 4 and Cost Metric 1], where each cycle is comparable to one (classical) hash function invocation. The number of logical-qubit-cycles is considered to be the total cost of the attack: $\text{COST}_{\text{lgc}} = \text{COST}_{\text{SCC}} \cdot (Q_G^{\text{log}} + Q_{MD}^{\text{log}})$. We consider this metric to be the most fitting in comparison to the time-space product given for the best generic attack in [10].

3 Reversible Implementations

We implemented² the Haraka and SHAKE-256 hash functions in Q#. We briefly review the schemes and describe our reversible implementations. To the best of our knowledge, this is the first reversible implementation of Haraka.

3.1 Haraka

Haraka, as specified in [20], consists of AES encryptions (*aesEnc*) and a mixing step (*mix*) for the permutation, which is used in turn to instantiate a sponge construction with a capacity of 256 bits, resulting in the Haraka-Sponge hash function. The Haraka512 hash function is defined as the truncated XOR of the input value and the output of the Haraka512 permutation on said input. Algorithm 4 describes the Haraka512 permutation. We partially reuse the AES implementation from [17] and adjust it to our use case.

For the AES encryption[21], we implement each of its four steps. The *SubBytes* step consists of applying the AES S-Box on each 8-bit block of the input. We use the implementation of [17] for the S-Box and additionally implement its inverse based on the proposed circuit in [5] using 120 ancillary qubits.

² <https://github.com/RobinBerger/Grover-Sphincs>

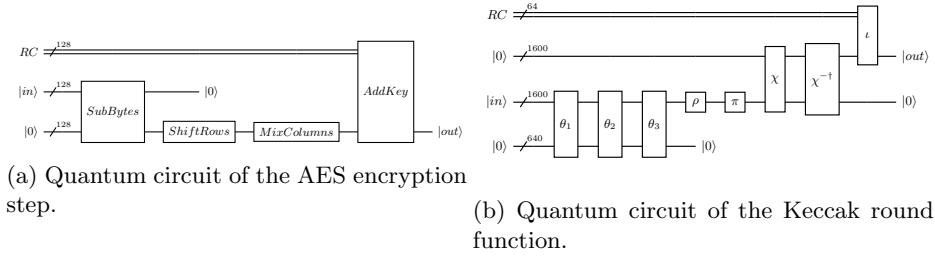


Fig. 2: Implementation of the round function components of Haraka and SHAKE-256.

This allows us to compute the output of the operation into new qubits and then using the adjoint inverse S-Box to reset the input qubits. In contrast to the implementation in [17], this allows us to recursively apply AES multiple times without needing additional qubits for every application, at the cost of additional quantum gates required. The *ShiftRows* step swaps qubits, thus we simply apply all following gates to different qubits (resulting in no additional cost). The *MixColumns* operation is the same implementation as the one by [17]. The *AddKey* operation is implemented using classically controlled NOT gates, as we use classical AES round keys, whereas [17] use quantum round keys. Fig. 2a shows the complete circuit for the AES encryption.

Similarly to the *ShiftRows* operation, we implemented the mixing step for the Haraka permutation by redirecting the quantum wires.

The AES encryption operation computes the output into a new set of qubits, freeing up the input qubits. We apply this twice on each input block, alternating the input and output qubits, followed by the mixing step. This completes the round function that is repeated a total of 6 times for the Haraka512 permutation.

We implement the Haraka512 hash function by *copying* the input into ancillary qubits using CNOT gates, then applying the Haraka512 permutation on these qubits. Next, the relevant qubits from the output of the permutation and the input of the hash function are XORed into the output qubits using CNOT gates. Finally, the ancilla qubits are freed up again by applying the adjoint Haraka512 permutation. The Haraka-based sponge construction is implemented by instantiating a sponge construction with the Haraka512 permutation.

The quantum gate count for our implementations of the Haraka permutation and hash function can be seen in Table 1. Note that Q# optimizes the width of the quantum circuit, reusing ancillary qubits whenever possible, even if this results in a significantly higher depth of the quantum circuit. As the exact amount of quantum gates required depends on the SPHINCS⁺ instance, all round constants for determining the gate count here and in the rest of this work are assumed to be zero. When using the default round constants, 2582 additional NOT gates are required for every application of the Haraka512 permutation, which is negligible compared to the gates required for the rest of the implementation.

Table 1: Resources for our implementation of the Haraka512 permutation and hash function. The width of the circuit includes the input and output qubits.

	T	CNOT	QubitClifford	T-Depth	Width
Permutation	609 289	1 383 040	189 440	69 125	1144
Hash Function	1 218 560	2 767 616	378 880	138 250	1912

Algorithm 5: KeccakPermutation($A[x][y][z] : \{0, 1\}, 0 \leq x, y < 5, 0 \leq z < 64$)

```

1 for  $0 \leq i < 24$  do
2    $A := \iota((\chi \circ \pi \circ \rho \circ \theta)(A), i)$ 
3 return  $A$ 

```

3.2 SHAKE-256

The SHAKE-256 hash function, as specified in [22], consists of the Keccak permutation, which is used to instantiate a sponge construction. The Keccak permutation consists of iterating the steps θ , ρ , π , χ and ι 24 times. The complete permutation is described in Algorithm 5, where the five steps are defined as

$$\begin{aligned}
\theta : \quad C[x][z] &:= \bigoplus_{0 \leq j < 5} A[x][j][z] \\
D[x][z] &:= C[x-1][z] \oplus C[x+1][z-1] \\
A'[x][y][z] &:= A[x][y][z] \oplus D[x][z] \\
\rho : \quad A'[x][y][z] &:= A[x][y][z + c[x][y]] \\
\pi : \quad A'[x][y][z] &:= A[x + 3y][x][z] \\
\chi : \quad A'[x][y][z] &:= A[x][y][z] \oplus ((A[x+1][y][z] \oplus 1) \cdot A[x+2][y][z]) \\
\iota : \quad A'[x][y][z] &:= \begin{cases} A[x][y][z] \oplus RC_i[z] & x = 0 \wedge y = 0 \\ A[x][y][z] & \text{otherwise} \end{cases} .
\end{aligned}$$

We note that our implementation follows closely the definition in [22] and thus has a similar structure to the one used by [1]. The operation θ is split into three parts $\theta_{1,2,3}$. θ_1 and θ_2 are a straight forward implementation of the SHA-3 specification, where we compute intermediate values in step θ_1 which are used in θ_2 to compute the output of the θ step. θ_3 implements θ^{-1} to uncompute intermediate values and is based on the KeccakTools reference implementation[2]. All XOR operations are implemented using CNOT gates. ρ and π are permuting the input and output bits by adjusting the subsequent quantum wires. The χ step of the Keccak permutation is a straight forward implementation of the specification with binary addition and multiplication based on CNOT and Toffoli gates, χ^{-1} is the respective inverse, where the adjoint χ^{-1} uncomputes the input qubits. This is the design also used by [1]. The ι step XORS a round constant on the state, which is implemented using classically controlled NOT gates.

Table 2: Quantum gate count for our implementation of the Keccak permutation and for the work by [1]. Gate counts for θ and χ are given for one round. Gate counts for ι , and the complete Keccak permutation are given for all 24 rounds.

	step	T	CNOT	QubitClifford	T-Depth	Width
Our implementation	$\theta_{1,2,3}$	0	63 040	0	0	2240
	χ	11 200	19 200	3200	25	3200
	χ^{-1}	13 440	23 360	3840	30	3200
	ι	0	0	86	0	1600
	Keccak	591 360	2 534 400	169 046	1176	3200
Implementation in [1]	Keccak	591 360	33 269 760	169 045	792	3200
	optimized	499 200	34 260 480	169 045	432	3200

The padding for the sponge construction is implemented using classically controlled NOT gates on the state.

The quantum circuit for the round function is represented in Fig. 2b. The Keccak permutation consists of applying this implementation 24 times while alternating input and output qubits.

The quantum gates for our implementation of the Keccak permutation and a comparison with [1] can be seen in Table 2. The most notable differences are that we use more than an order of magnitude fewer CNOT gates, because we use ancilla qubits for the θ operation and that we use the T-depth 5 Toffoli gate provided by Q# while [1] use a T-depth 3 Toffoli gate.

4 Attacking the SPHINCS⁺ Signature Scheme

We analyzed the WOTS, FORS and XMSS components of the SPHINCS⁺ scheme to identify weak points and compared resources to mount an attack. Briefly speaking, we determined that forging an XMSS signature requires the fewest logical resources to forge a complete SPHINCS⁺ signature. In the following sections we describe two of our attacks in more detail.

4.1 Forging a SPHINCS⁺ Signature on the XMSS component

To compute a universal forgery for a signature of a message \tilde{m} , we create a new SPHINCS⁺ instance associated with a secret key $\tilde{\text{sk}}_{\text{SPHINCS}^+}$. The root node of the topmost XMSS instance of our new hypertree evaluates to the original public key $\text{pk}_{\text{SPHINCS}^+}$ as in Fig. 3.

Let $\tilde{\sigma}_{\text{SPHINCS}^+}^{\tilde{m}} := (\tilde{\sigma}_{\text{FORS}}^{\tilde{m}}, \tilde{\sigma}_{\text{HT}}^{\text{pk}_{\text{FORS}}})$ be a forged signature. The FORS signature is a freshly generated signature and the validation of which depends only on the new key pair. To forge the signature $\tilde{\sigma}_{\text{HT}}^{\text{pk}_{\text{FORS}}}$ we use the public key of the topmost WOTS instance generated from $\tilde{\text{sk}}_{\text{SPHINCS}^+}$ and replace the respective XMSS signature with a forged signature $\tilde{\sigma}_{\text{XMSS}}$: Therefore, we need to find an

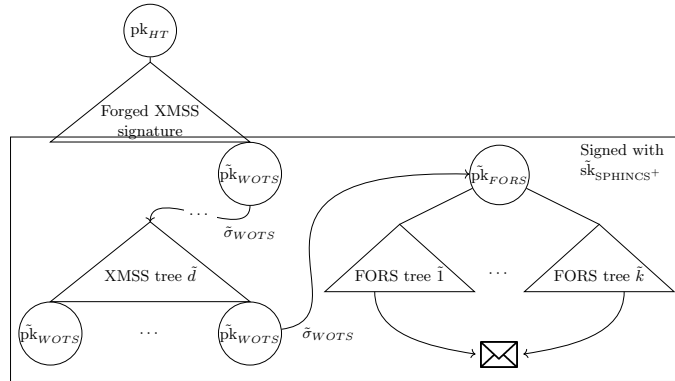


Fig. 3: Forged SPHINCS⁺ signature using a forged XMSS signature.

authentication path $\tilde{\text{path}}_{XMSS}$ in the respective tree, so that computing the root node of the tree along the path with the respective WOTS public key results in the given XMSS public key.

Let $p_1 \dots p_{h'}$ be the nodes on the path from the given WOTS public key node to the root of the XMSS tree with p_1 being the leaf and $p_{h'}$ being the root node. Also, let $v_1 \dots v_{h'-1}$ be the respective sibling nodes. p_1 is the WOTS public key and p_i is computed from p_{i-1} and v_{i-1} for $i > 0$.

To find values v_i for an authentication path, we select the first $h' - 2$ values $v_1 \dots v_{h'-2}$ at random from $\{0, 1\}^n$. This results in fixed values $p_1 \dots p_{h'-1}$. Then we can forge the authentication path path_{XMSS} if we can find a value $v_{h'-1}$ to complete the path. We can estimate the probability of such a preimage $v_{h'-1}$ existing for a fixed $v_1 \dots v_{h'-2}$ and a given public key, if we assume that the deployed hash function behaves like a random oracle, i.e. with each value $\mathcal{H}(x)$ being chosen uniformly at random independently from each other:

$$\begin{aligned} \mathbb{P}(\exists x \in \{0, 1\}^n : \mathcal{H}(x) = pk) &= 1 - \mathbb{P}(\forall x \in \{0, 1\}^n : \mathcal{H}(x) \neq pk) \\ &\geq 1 - \frac{1}{e} \end{aligned} \tag{1}$$

This means that a preimage $v_{h'-1}$ exists with probability $\geq 1 - 1/e$. Therefore, forging a valid signature for a message depends only on finding the value $v_{h'-1}$. In the remaining paper we are concerned with estimating the resources to find this value using Grover's algorithm on a fault-tolerant quantum computer.

While this attack can be modified by generating WOTS instances for one half of the attacked XMSS instance, allowing to easily forge signatures for multiple messages if they fall on that side of the XMSS tree, the setup and the cost for the preimage search is the same, so we will not go into more detail with this.

Resource Estimate To forge the XMSS signature, we need to find a preimage of the Haraka-based sponge or the SHAKE-256 hash function using Grover's algorithm. In the following estimate, let n be the security parameter in bits.

Table 3: Gate count for our implementation of the Grover components in one Grover iteration.

	T	CNOT	QubitClifford	T-Depth	Width
SPHINCS ⁺ -128-Haraka	2 438 891	5 535 202	758 282	275 713	1400
SPHINCS ⁺ -128-SHAKE-256	1 184 491	5 071 842	338 614	3635	3456
SPHINCS ⁺ -256-Haraka	2 440 683	5 538 274	758 794	276 865	1656
SPHINCS ⁺ -256-SHAKE-256	1 186 283	5 076 450	339 126	4787	3712
Grover Diffusion (128 bit)	1771	2530	1022	1139	–
Grover Diffusion (256 bit)	3563	5090	2046	2291	–

Table 4: Resource estimate for a preimage search to forge an XMSS signature, where the target column indicates if the left or right node of the hash tree is attacked.

SPHINCS ⁺ instantiation	Gate count	T-Depth	T-Depth-Times-Width
SPHINCS ⁺ -128-Haraka	$1.6 \cdot 2^{86}$	$1.7 \cdot 2^{81}$	$1.1 \cdot 2^{92}$
SPHINCS ⁺ -128-SHAKE-256	$1.2 \cdot 2^{86}$	$1.8 \cdot 2^{75}$	$1.5 \cdot 2^{87}$
SPHINCS ⁺ -256-Haraka	$1.6 \cdot 2^{150}$	$1.7 \cdot 2^{145}$	$1.4 \cdot 2^{156}$
SPHINCS ⁺ -256-SHAKE-256	$1.2 \cdot 2^{150}$	$1.4 \cdot 2^{140}$	$1.2 \cdot 2^{152}$

For the Haraka instantiation, the input to the hash function consists of a 256 bit address and two n -bit values, one of which is the hash value of a node in the XMSS tree, the other one is the value searched for by Grover’s algorithm to forge the signature. For the SHAKE-256 instantiation, the input to the hash function consists of a n -bit public key seed and the same inputs as with Haraka.

Using $n = 128$ for the Haraka instantiation, we can save resources by precomputing one iteration of the Haraka512 permutation. As the rate of the sponge instantiation is 256 bits, the first iteration absorbing the address can always be precomputed, so the quantum circuit is implemented using a different initial state, skipping this iteration. Using the same security parameter for the SHAKE-256 instantiation, none of the iterations can be precomputed. The gate count for the implementation of these Grover oracles as well as for the Grover diffusion operator for the SPHINCS⁺-128 and SPHINCS⁺-256 parameter sets as determined by Q# are shown in Table 3. While we include the 256-bit parameter sets for comparison, we want to note that for the Haraka hash function, more efficient attacks exist for that parameter set.

For $n = 128$, Grover’s algorithm requires roughly $1.6 \cdot 2^{63}$ iterations. Combining these gate counts with the amount of Grover iterations, we can evaluate two cost metrics for this attack. These results are shown in Table 4.

We can see that the attack using the SHAKE-256 hash function performs better on both cost metrics than the attack using Haraka. This results from the additional iterations of the Haraka permutation compared to SHAKE-256.

4.2 Forging a SPHINCS⁺ Signature on the WOTS Component

An alternate approach to forging SPHINCS⁺ signatures is to attack the WOTS component. Similarly to the previous attack, this is a universal forgery attack, however we also require a message m , that already has a valid signature σ .

The general attack strategy is similar to [9], i.e. the selection of the WOTS instance and the construction of the SPHINCS⁺ signature from the other components: We generate a SPHINCS⁺ signature for a new message using a new secret key, making sure that this signature uses the first-layer WOTS instance at the same position as the one in σ . We then forge a WOTS signature, that authenticates our second-layer XMSS public key for the first-layer WOTS public key in the original structure. In comparison to [9], who use a fault injection attack, we forge the WOTS signature using a quantum preimage attack.

Custom Selection of WOTS Instances Similarly to [9], when creating a SPHINCS⁺ signature σ^* for a message m^* , we need to use a FORS instance, that results in σ^* using the first-layer WOTS instance at the same position as σ does. They state that this is possible on a classical computer with feasible effort. In the setting of SPHINCS⁺-128 [16], this takes an average of $\approx 2^9$ hash function invocations.

Forging WOTS Signatures As the WOTS signature scheme divides a message into message and checksum blocks and then signs each block individually, forging a WOTS signature requires forging a signature for each block. A signature for a block containing a message m_i consists of the m_i -th element of a hash chain.

Let m_i and m_i^* be the message in the i -th block of m and m^* respectively. For $m_i \leq m_i^*$, a signature for block i can be computed, by advancing in the hash chain $m_i^* - m_i$ times by applying the hash function. For $m_i > m_i^*$, we need to go back $m_i - m_i^*$ times in the hash chain. To do this, we can apply Grover's algorithm.

Such a preimage to a value of the hash chain must exist, as σ was generated using this value. As it might not be unique, but only the value used to generate σ is guaranteed to have a preimage again, this means that instead of applying Grover's algorithm multiple times, to go back in the hash chain once in each step, we need to do a preimage search on a recursive application of the hash function in a single step. If multiple preimages of the recursive application of the hash function exist, any one of them produces a valid signature for that block.

Let the length of each block be $\log_2 w$. As the messages for the WOTS signature scheme are outputs of a hash function and therefore the message blocks m_i and m_i^* are blocks of an output of a hash function, it is reasonable to assume, that they are distributed uniformly at random from the set $\{0, \dots, w - 1\}$, independently from each other. However we cannot assume this assumption to hold for the checksum blocks. Using this assumption for the message blocks, we can estimate the recursion depth of the hash function required for this attack. We

Table 5: Gate count required by our implementation of the Grover oracles for a recursion depth 5 of the hash functions.

	T	CNOT	QubitClifford	T-Depth	Width
SPHINCS ⁺ -128-Haraka	12 187 371	27 667 810	3 789 310	1 369 814	1912
SPHINCS ⁺ -128-SHAKE-256	11 828 971	50 694 370	3 381 550	26 098	3968
SPHINCS ⁺ -256-Haraka	12 189 163	27 674 850	3 789 822	1 357 142	2680
SPHINCS ⁺ -256-SHAKE-256	11 830 763	50 700 770	3 382 062	27 250	4736
Grover Diffusion (128 bit)	1771	2530	1022	1139	–
Grover Diffusion (256 bit)	3563	5090	2046	2291	–

will follow a simple approach, only considering a single preimage search for a message block, neglecting the amount of preimage searches required for checksum blocks and the possibility of searching for weak instances. A more detailed approach also considering the aforementioned aspects is beyond the scope of this work.

For the recursion depth required for a preimage search we take the value d , so the probability of a recursion depth of $\geq d$ and $\leq d$ being required for the preimage search is $\geq 1/2$. For the SPHINCS⁺ parameters proposed in [16] with $\log w = 4$, this results in $d = 5$.

Resource Estimate For forging a WOTS signature and carrying out this attack, we need to do multiple preimage searches of a recursive application of the SHAKE-256 or Haraka512 hash function. Let n be the security parameter in bits in the following resource estimate.

For the Haraka instantiation, the input to the hash function consists of a 256 bit address and the n bit value searched for. The SHAKE-256 instantiation additionally gets an n bit public key seed as input.

Using $n = 128$, we will only go into detail for a hash function recursion depth of 5, as calculated previously. The gate count required for the Grover oracles for this attack for both of the hash functions and for the diffusion operator are shown in Table 5.

As with the previous attack, for $n = 128$, $\approx 1.6 \cdot 2^{63}$ Grover iterations are required for one preimage attack. We can again combine this with the gate counts from Table 5 to evaluate the cost metrics for this attack. This is shown in Table 6. As mentioned previously, the cost metric does not capture that this attack requires multiple preimage attacks of variable recursion depths.

As the Haraka512 hash function is used here and not the Haraka-Sponge hash function used in the previous attack, the amount of applications of the underlying permutation is the same for the Haraka and SHAKE-256 instantiation, with the Haraka permutation requiring fewer quantum gates explaining the results of the gate count metric. As in the previous attack, Haraka performs worse in the T-Depth-Times-Width metric, as the Haraka permutation has a significantly higher T-Depth.

Table 6: Resource estimate for a preimage search to forge a WOTS signature.

SPHINCS ⁺ instantiation	Gate count	T-Depth	T-Depth-Times-Width
SPHINCS ⁺ -128-Haraka	$1.0 \cdot 2^{89}$	$1.0 \cdot 2^{84}$	$1.9 \cdot 2^{94}$
SPHINCS ⁺ -128-SHAKE-256	$1.5 \cdot 2^{89}$	$1.3 \cdot 2^{78}$	$1.3 \cdot 2^{90}$
SPHINCS ⁺ -256-Haraka	$1.0 \cdot 2^{153}$	$1.0 \cdot 2^{116}$	$1.3 \cdot 2^{159}$
SPHINCS ⁺ -256-SHAKE-256	$1.5 \cdot 2^{153}$	$1.4 \cdot 10^{142}$	$1.6 \cdot 2^{154}$

5 Fault-tolerant Cost

In this section, we give tight cost estimates of carrying out the *most promising* attack on XMSS signatures in Section 4.1. In particular, we analyze the resource requirements for the SPHINCS⁺-128 parameter sets, i.e. Haraka and SHAKE-256 hash function. A comparison of all results can be found in Table 7. The analysis follows the approach by [1], but optimizes the parallelization of the magic state distillation.

5.1 Haraka

Setup The entire Grover circuit for the attack using the Haraka hash function consists of $T_{\text{Haraka}} = 3.54 \cdot 10^{25}$ T-gates, $G_{\text{Haraka}}^{\text{cnot}} = 8.02 \cdot 10^{25}$ CNOT-gates and $G_{\text{Haraka}}^c = 1.1 \cdot 10^{25}$ QubitClifford-gates with the Hadamard-gates dominating, thus other types of gates are ignored. The circuit has a width of $Q_{\text{Haraka}}^w = 1400$ and a T-depth of $T_{\text{Haraka}}^d = 4.01 \cdot 10^{24}$.

Magic State Distillation Given the desired output error rate relative to the size of the circuit $p_{\text{out}} = 1/T_{\text{Haraka}}^d$ and the assumptions given in Section 2.4 one can determine the number of layers of magic distillation required. We require two layers as in Fig. 4a, each with a surface code distance d_i , number of logical Q_i^{log} and respectively physical qubits Q_i^{phy} . In total, the number of logical qubits for a single distillery is $Q_{\text{MD, Haraka}}^{\text{log}} = 240$.

The layers can be optimized based on the cost metrics from Section 2.4, i.e. $\text{COST}_{\text{IQC}} = \text{COST}_{\text{SCC}} \cdot (Q_G + Q_{\text{MD}})$ and $\text{COST}_{\text{SCC}} = \text{SCC} \cdot G^d$, thus increasing the number of cycles scales the cost by both, cycles and qubits. Consider an increase of cycles by a factor X and an increase of qubits by a factor Y . Then the optimal distillery can be found by computing $\min_{X,Y} XQ_G + XYQ_{\text{MD}}$.

Surface Code The gates in the circuit are embedded into a surface code of distance $D_{G,\text{Haraka}} = 25$, with $p_{\text{out}} = 1/(G_{\text{Haraka}}^{\text{cnot}} + G_{\text{Haraka}}^c)$ as targeted error rate. This results in each of the $Q_{G,\text{Haraka}}^{\text{log}} = 1400$ logical qubits to require 1352 physical qubits. In total, the algorithm requires $Q_{G,\text{Haraka}}^{\text{phy}} \approx 1.89 \cdot 10^6$ physical qubits.

Results On average, about 9 T-gates are applied in each layer of T-depth. The number of physical qubits is dominated by the surface code, therefore we suggest to compute all magic states in parallel using 3 magic state distilleries and

- (a) Distance of the error correcting code d_i and number of logical and physical qubits for each layer i .
- (b) Pipelining the production of 3 magic states allows to reuse the qubits from the bottom layer in the top layer.

Layer	i	d_i	Q_i^{\log}	Q_i^{phy}
Top	1	19	16	12800
Bottom	2	9	240	48000

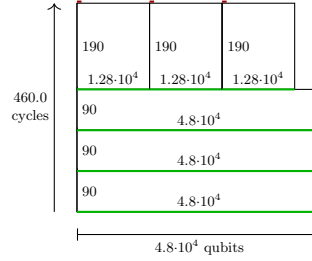


Fig. 4: Magic state distillation scheme for attacking SPHINCS⁺-128.

$Q_{MD}^{\text{phy}} = 3 \cdot 48000 = 1.6 \cdot 10^4$ physical qubits in $\text{SCC}_{\text{Haraka}}^m = 460$ cycles. The average number of gates per layer of T-depth for each CNOT and QubitClifford gates, $G_{\text{Haraka}}^{\text{cnot}} / Q_{\text{Haraka}}^w \cdot T_{\text{Haraka}}^d \approx 0.0143$ and $G_{\text{Haraka}}^c / Q_{\text{Haraka}}^w \cdot T_{\text{Haraka}}^d \approx 0.002$, is significantly smaller than the number of surface code cycles required to implement a single layer required for magic state distillation.

Therefore, the total number of surface code cycles for the entire algorithm is dominated by the magic state distilleries, which is $\text{COST}_{\text{SCC}} = \text{SCC}_{\text{Haraka}}^m \cdot T_{\text{Haraka}}^d = 460 \cdot 4.01 \cdot 10^{24} \approx 1.5 \cdot 2^{90}$. The total number of logical qubits required is 2120. With 200ns per surface code cycle, this would take $1.17 \cdot 10^{13}$ years. The total cost of running the attack is then $\text{COST}_{\text{lqcHaraka}} = \text{COST}_{\text{SCC}} \cdot (1400 + 3 \cdot 240) = 1.5 \cdot 2^{90} \cdot (2120) \approx 3.91 \cdot 10^{30} \approx 1.55 \cdot 2^{101}$.

5.2 SHAKE-256

Setup When using the SHAKE-256 hash function, our quantum circuit for the entire Grover algorithm for the attack contains $G_{\text{SHAKE-256}}^T = 1.72 \cdot 10^{25}$ T-gates and $G_{\text{SHAKE-256}}^{\text{cnot}} = 7.35 \cdot 10^{25}$ CNOT-gates. It also contains $G_{\text{SHAKE-256}}^c = 4.92 \cdot 10^{24}$ QubitClifford gates, most of which are Hadamard-gates. We will ignore any QubitClifford gates, that are not Hadamard-gates. The quantum circuit has a logical width of $Q_{\text{SHAKE-256}}^w = 3456$ qubits and a T-Depth of $T_{\text{SHAKE-256}}^d = 6.92 \cdot 10^{22}$.

Magic State Distillation The number of layers and thus the values for magic state distillation are reminiscent to those of Section 5.1, in particular, of Fig. 4a.

Surface Code The distance of the surface code remains as $D_{G,\text{SHAKE-256}} = 25$, with the same targeted error rate. This results in each of the $Q_{G,\text{SHAKE-256}}^{\log} = 3456$ logical qubits to require 1953 physical qubits. In total the algorithm requires $Q_{G,\text{SHAKE-256}}^{\text{phy}} \approx 6.75 \cdot 10^6$ physical qubits.

Results On average, about 249 T-gates are applied in each layer of T-depth. Therefore, we suggest to use 83 distilleries each generating 3 states in parallel,

Table 7: Fault-tolerant cost for our attack from Section 4.1 using the SHAKE-256 and Haraka hash functions. The collision attack of [10] refers to attacking the internal state of Haraka.

SPHINCS ⁺ -		SHAKE-256		Haraka
Collision Attack [10]	#Grover Iterations		–	$1.32 \cdot 2^{102}$
	Time-Space Product		–	$1.51 \cdot 2^{153}$
	#Classical hash function invocations		–	$2^{129.5}$
Our Attack on 128	#Distilleries	ϕ	83×3	3×3
	#Log. Qubits	Q^{\log}	23876	2120
	#Total Phys. Qubits	Q^{phy}	$8.65 \cdot 10^6$	$2.03 \cdot 10^6$
	#Total ECC cycles	COST_{SCC}	$1.6 \cdot 2^{84}$	$1.5 \cdot 2^{90}$
	logical-qubit-cycles	COST_{lqc}	$2.65 \cdot 2^{99}$	$1.55 \cdot 2^{101}$
Our Attack on 256	#Distilleries	ϕ	42×4	9×1
	#Log. Qubits	Q^{\log}	$1.7 \cdot 10^5$	$0.38 \cdot 10^5$
	#Total Phys. Qubits	Q^{phy}	$5.8 \cdot 10^7$	$1.5 \cdot 10^7$
	#Total ECC cycles	COST_{SCC}	$1.02 \cdot 2^{152}$	$3.95 \cdot 2^{154}$
	logical-qubit-cycles	COST_{lqc}	$1.31 \cdot 2^{169}$	$1.44 \cdot 2^{171}$

using a total of $Q_{MD}^{\text{phy}} = 83 \cdot 48000 = 3.98 \cdot 10^6$ physical qubits in $\text{SCC}_{\text{SHAKE-256}}^m = 460$ cycles. The average number of CNOT gates per layer of T-depth is 0.31, and 0.021 Hadamard gates.

Again, magic state distillation dominates resulting in a total number of $\text{COST}_{\text{SCC}} = \text{SCC}_{\text{SHAKE-256}}^m \cdot T_{\text{SHAKE-256}}^d = 460 \cdot 6.92 \cdot 10^{22} \approx 1.6 \cdot 2^{84}$ surface code cycles.

The total number of logical qubits required is 23876. With $200ns$ per surface code cycle, this would take $2.02 \cdot 10^{11}$ years. The total cost of running the attack is then $\text{COST}_{\text{lqcSHAKE-256}} = \text{COST}_{\text{SCC}} \cdot (3456 + 83 \cdot 240) = 1.6 \cdot 2^{84} \cdot (23876) \approx 7.44 \cdot 10^{29} \approx 2.65 \cdot 2^{99}$.

6 Conclusion

We presented quantum implementations for the Haraka (and respectively SHAKE-256) hash function in the context of the SPHINCS⁺ signature scheme. Subsequently, we proposed and reviewed multiple points of attack in the SPHINCS⁺-128-Haraka signature scheme based on applying Grover’s algorithm to find pre-images. A tight estimate of the resources required to carry out the most promising attack on a fault tolerant quantum computer is given. Our attack, that forges a signature in $1.55 \cdot 2^{101}$ steps, improves over the previously best known attack on SPHINCS⁺-128-Haraka.

Following the suggestion by NIST to review the security in terms of a maximal depth for quantum circuits, it is clear that for a depth of 2^{96} the attack can be

implemented without any further constraints and would be more efficient than the classical counter part. For a depth of 2^{40} and 2^{64} the overhead induced by error correction needs to be reevaluated and optimized to the respective depth. A detailed analysis is out of scope for this paper and left as future work.

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