

Upgrading the Beam Telescopes at the DESY II Test Beam Facility

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Abstract

The DESY II Test Beam Facility is a key infrastructure for modern high energy physics detector development, providing particles with a small momentum spread in a range from 1 to 6 GeV to user groups e.g. from the LHC experiments and Belle II as well as generic detector R&D. Beam telescopes are provided in all three test beam areas as precise tracking reference without time stamping, with triggered readout and a readout time of $> 115 \mu\text{s}$. If the highest available rates are used, multiple particles are traversing the telescopes within one readout frame, thus creating ambiguities that cannot be resolved without additional timing layers. Several upgrades are currently investigated and tested: Firstly, a fast monolithic pixel sensor, the TELEPIX, to provide precise track timing and triggering on a region of interest is proposed to overcome this limitation. The TELEPIX is a 180 nm HV-CMOS sensor that has been developed jointly by DESY, KIT and the University of Heidelberg and designed at KIT. In this publication, the performance evaluation is presented: The difference between two amplifier designs is evaluated. A high hit detection efficiency of above 99.9% combined with a time resolution of below 4 ns at negligible pixel noise rates is determined. Finally, the digital hit output to provide region of interest triggering is evaluated and shows a short absolute delay with respect to a traditional trigger scintillator as well as an excellent time resolution. Secondly, a fast LGAD plane has been proposed to provide a time resolution of a few 10 ps, which is foreseen to drastically improve the timing performance of the telescope. Time resolutions of below 70 ps have been determined in collaboration with the University of California, Santa Barbara.

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1. Introduction

The development of novel particle detectors, be it for example upgrades of the LHC experiments, Belle II or more generic research in the context of next generation high energy physics experiments, relies crucially on tests under realistic conditions. Test beam facilities like the one at DESY II [1] are optimally suited for this purpose. The characterization of detector prototypes in a test beam requires precise knowledge of 4D track information, which is typically provided by beam telescopes. At

the DESY II Test Beam Facility, three EUDET-type pixel beam telescopes [2] provide an unprecedented pointing resolution of down to $2 \mu\text{m}$. This is achieved by the small pitch (ca. $18 \mu\text{m}$) of the MIMOSA-26 [3, 4] pixel sensors and an ultra-low material budget of 0.07% of a radiation length per tracking layer. Each of the six sensors is read out in a rolling shutter mode with the duration of one readout cycle of $115 \mu\text{s}$. A TLU [5] generates triggers based on a scintillator coincidence and exchanges trigger numbers with connected devices, allowing for data synchronization on hardware level. The maximum trigger rate that can be processed by the system is 3 kHz while the available particle rates can go up to several 10 kHz. Therefore, data frames with multiple hits per sensor creating ambiguities will occur. The main drawback of the MIMOSA-26 sensors is the lack of additional hit time information. Extending the EUDET-type telescopes by an additional pixelated sensor that adds a precise time stamp to the spatial information allows for timing studies of devices under test (DUTs). This is crucial, e.g. to study if hits can be reliably assigned to the correct LHC bunch crossing. In early prototyping phases, the active area of a DUT is often small compared to the size of the MIMOSA-26 sensors ($2 \times 1 \text{ cm}^2$). In this case, an additional pixelated layer can also resolve the above-mentioned ambiguities by implementing a configurable region-of-interest (ROI) trigger output, that rejects particles outside the DUTs acceptance. This allows for

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efficient data taking with low rate DAQ systems.

None of the already available sensors at the test beam combine a fine enough time stamp with trigger capabilities. In addition, they represent a significant amount of material when put in the beam, due to their hybrid design.

A test chip, the TELEPIX, in a 180 nm HV-CMOS technology is developed to overcome these limitations, by providing a time stamp with a precision below 5 ns, combined with a fast digital hit output signal allowing for a configurable region of interest (ROI) trigger.

Another approach that is currently under investigation is the use of low gain avalanche diodes (LGADs) to provide timing with a precision of a few 10 ps, where single channels can also be used as a coarse ROI trigger for the telescope readout.

2. TELEPIX as a Timing and Triggering layer

2.1. Sensor Design

The TELEPIX chip layout is based on previous submissions, which are described elsewhere [6, 7]. It comprises 29x124 pixels with a pitch of 165x25 μm^2 . TELEPIX is part of a full reticle submission with several test chips that has been received in late autumn 2020. Signals are amplified inside the active pixel and digitized in the periphery at one edge of the sensor. Both the time-of-arrival (ToA, 10 bit) and time-over-threshold, (ToT, 10 bit) are registered in the periphery. The ToA is sampled on both clock edges, which doubles the timestamp precision without increasing the required clock frequencies. TELEPIX implements two different amplifiers with a p-mos (n-mos) input transistor for columns 0-14 (15-29). Each part has its own global externally applied threshold. The sensor can be biased to up to -80 V to create a thick active depletion layer. Each pixel can be switched off individually, while the top half of the sensor features a 3 bit threshold trimming. The chip operates trigger-less and continuously streams out data at 1.25 Gbit/s. Finally, TELEPIX features a fast digital trigger output option with a configurable pixel column range called Hrr-Bus.

2.2. Test Beam characterization

The existing DAQ framework [8, 9] of the Mu3e collaboration is used to configure and read out TELEPIX. All investigation results shown in the following are obtained at the DESY II test beam facility. The sensor bias is set to -70 V if not stated otherwise. Figure 1 depicts the setup used to characterize the sensor in beam. The TELEPIX is placed between two triplets of a provided reference telescope, which is framed by two scintillators generating triggers to activate the telescopes' readout. Telescope and TELEPIX are synchronized with an AIDA-TLU [5]: The telescope exchanges trigger IDs with the TLU, while the TELEPIX DAQ receives a phase stable 125 MHz clock from the TLU and a reset signal at run start. The reset signal sets all counters in TELEPIX to zero. Automated data taking is realized via a full integration into EUDAQ2[10].

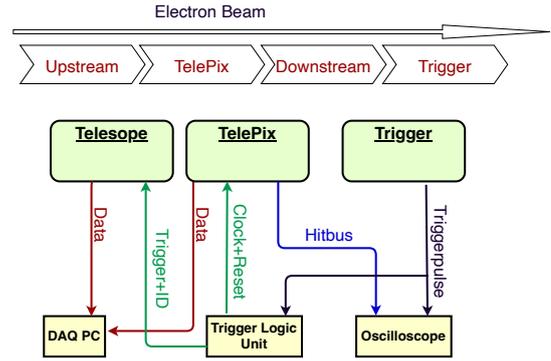


Figure 1: Test setup during the test beam campaign. For triggering tests, the scintillator and digital hit out are connected to a 4 GHz oscilloscope.

The recorded data is analyzed with CORRYVRECKAN [11]: Around each TLU trigger, a time window with $\pm 10\ \mu\text{s}$ is created and all TELEPIX hits with matching timestamps are added to the frame. Subsequently, clustering in space (and time for the TELEPIX) is performed. The particle trajectories are reconstructed based on the General-Broken-Lines formalism [12], with the timestamp of the track being defined by the trigger time stamp from the TLU. Only events with a single reference trajectory are kept for analysis, reducing the available data by a few percent, while providing a very clean sample. Clusters on the TELEPIX, that are within 200 μm around the particle intersection point and $\pm 500\text{ ns}$ around the track timestamp are assigned to the tracks and kept for further analysis. A constant time offset of the TELEPIX is subtracted in the search window.

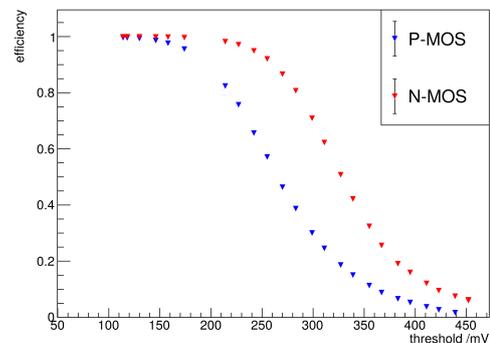


Figure 2: Hit detection efficiency as function of the detection threshold for the p-mos (blue) and n-mos (red) amplifiers. Note that statistical errors are included, but too small to be visible. A bias of -70 V is applied

Hit Detection Efficiency

The hit detection efficiency is defined as the number of tracks, that have an assigned cluster over the total number of reconstructed tracks, that intersect the DUT. Figure 2 shows the efficiency as a function of the externally applied detection threshold. Below 100 mV thresholds, the readout saturates due to high rates of noise induced pixel hits. Detection efficiencies of above 99.9 % are determined for the lowest thresholds reached in this

study. After a highly efficient region, that is significantly larger for the n-mos type, efficiency decreases firstly at the corners and edges due to charge sharing, that causes less available charge per individual pixel. For even higher thresholds, the efficiency also decreases in the center of the pixels.

Time Resolution

The time difference to the trigger scintillators is shown in figure 3 for a low threshold of 108 mV. A fit to the distribution's core results in a time resolution of 3.16(1) ns, without correcting for the time resolution of the scintillator signal. The entries in the tail of the distribution are explained by events in pixel corners with cluster size > 1 , with significant charge sharing and therefore less charge per pixel causing a longer delay for crossing the detection threshold, so called time walk.

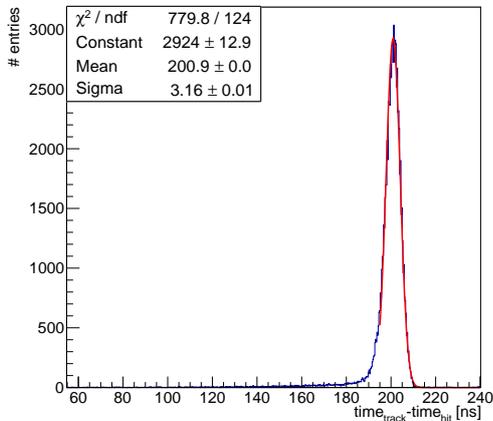


Figure 3: Exemplary time resolution of the n-mos amplifier section at a detection threshold of 108 mV. The tail towards the left is introduced by charge sharing and can be assigned to clusters with a width of two.

2.3. Region of Interest Triggering Capabilities

The trigger capabilities have been studied with a similar setup as depicted in figure 1. A 2x1 cm² plastic scintillator, read out via a PMT is placed closely behind the TELEPIX. Both, the HITBUS and the PMT, are connected to an oscilloscope, which is triggered on the HITBUS and the absolute latency between the leading edges of the HITBUS and scintillator pulse is histogrammed directly with an oscilloscope. Due to a design flaw, the fast logic OR can only be used in columns without any pixels being masked. Hence, a high threshold of 151 mV is used to suppress any effect from noisy pixels. Figure 4 shows the results for three bias voltages, with a Gaussian fitted to the core of the distributions while table 1 summarizes the results. Delays (Resolutions) from 22.55(3.81) ns to 28.04(6.56) ns are measured and do agree with the expected behaviour for increasing bias voltage: the average signal size increases as the depletion region grows and less fluctuations on the threshold crossing time and a shorter signal delay are the consequence. In addition, the detector capacity is reduced for higher bias creating less noise. All measured delays are acceptably short to be feasible as a trigger input for the TLU, which takes approximately

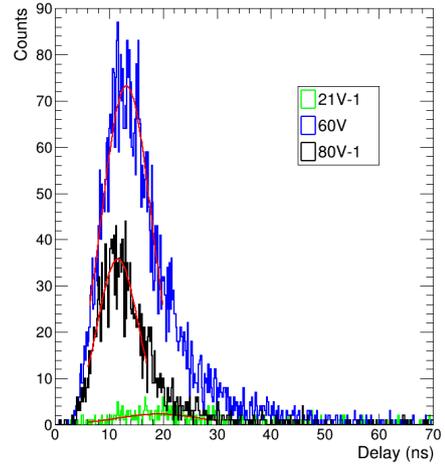


Figure 4: The measured time resolution and delay of the fast hit-or signal relative to a trigger scintillator coincidence with 4 GeV electrons.

Bias / V	resolution / ns	delay / ns
21	6.56±0.65	28.04±0.59
60	4.69±0.13	23.17±0.10
80	3.81±0.17	22.55±0.13

Table 1: . Summary of the time resolution as well as the absolute delay between scintillator and HitBus. Note that differences in the cable delays have been taken into account.

180 ns to process a trigger. The time resolution is consistent with the measurements presented above, if the higher threshold is taken into account.

2.4. LGAD Timing Plane

The proposed LGAD timing layer [13, 14] is based on two LGADs with 5x5 pixels produced by HPK, featuring JTE separation between segments and a pitch of 1.3x1.3 mm². During a first test beam, the LGADs have been characterized to demonstrate their timing capabilities, by placing two layers behind each other and triggering on any pixel hit of the front LGAD layer. The full analog waveform has been stored on four DRS4 boards [15], that have been synchronized to each other. The trigger ID has been sampled as an individual waveform in addition, due to the required bandwidth and buffer depth, only the six least significant bits of the ids could be recorded. The synchronization between telescope and LGADs could be verified in a recent test beam campaign. The time resolution of a single LGAD plane is extracted by the time difference between the two layers, if both see the particle, compare figure 5. A time resolution of 77.9(2) ps has been determined in an offline analysis for two LGADs with a bias voltage of 150V, resulting in an average individual LGAD time resolution of 55 ps.

The next generation readout, based on a 16 channel CAEN digitizer and two fast crystals as reference system is being tested at the time of writing.

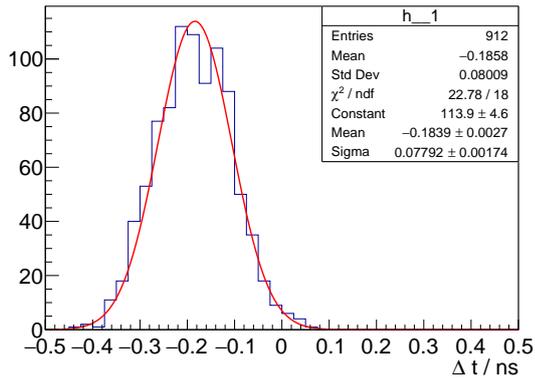


Figure 5: The measured time resolution of two LGAD layers relative to each other, measured with 5.8 GeV electrons.

3. Summary & Outlook

Both telescope timing layer upgrades are well on track and show promising results:

The TELEPIX shows an excellent hit detection efficiency of above 99.9 % at a bias voltage of -70 V. A time resolution of 3.16(1) ns has been determined without any further offline corrections. The HRTBus logic can be used as a ROI trigger and shows small delays of down to 32 ns with respect to a trigger scintillator.

Given the very good experiences and results from the presented TELEPIX prototype, a full scale prototype will be submitted to match the active area of the telescopes sensor planes. This is a low-risk step as the design is well advanced and takes advantage of the experience with e.g. the full-size ATLASPIX3 [6] that has been designed and produced in the same 180 nm process.

The time resolution of a coincidence of two LGADs has been determined to be 77.9(2) ps. A more evolved and integrated DAQ system is currently being tested and installed. A full integration into the telescope DAQ system is foreseen for this year. The timing layer upgrades will enhance the performance of the EUDET-type telescopes significantly and prepares the telescope for the coming challenges of future test beam research.

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