

PAPER • OPEN ACCESS

Annealing behaviour of charge collection of neutron irradiated diodes from 8-inch p-type silicon wafers

To cite this article: Oliwia Agnieszka Kaluzińska *et al* 2025 *JINST* **20** P06054

View the [article online](#) for updates and enhancements.

You may also like

- [A review of advances in pixel detectors for experiments with high rate and radiation](#)
Maurice Garcia-Sciveres and Norbert Wermes
- [4D tracking with ultra-fast silicon detectors](#)
Hartmut F-W Sadrozinski, Abraham Seiden and Nicolò Cartiglia
- [TCAD modeling of bulk and surface radiation damage effects in silicon devices](#)
F. Moscatelli, A. Morozzi, T. Croci *et al.*



ECS The Electrochemical Society
Advancing solid state & electrochemical science & technology

250
ECS MEETING CELEBRATION

*Step into the
Spotlight*

**SUBMIT YOUR
ABSTRACT**

250th ECS Meeting
October 25–29, 2026
Calgary, Canada
BMO Center

Submission deadline:
March 27, 2026

Annealing behaviour of charge collection of neutron irradiated diodes from 8-inch p-type silicon wafers

Oliwia Agnieszka Kałuzińska ^{a,b,*} Leena Diehl ^a Eva Sicking ^a
Marie Christin Mühlwinkel ^{a,c} Pedro Gonçalo Dias de Almeida ^{a,d} Jan Kieseler ^{a,e}
Matthias Kettner ^{a,f} David Walter ^a and Matteo M. Defranichis ^a

^aExperimental Physics Department, CERN,
Espl. des Particules 1, Geneva, Switzerland

^bETIT, Karlsruhe Institute of Technology,
Engelbert-Arnold-Straße 4, Karlsruhe, Germany

^cKirchhoff Institute for Physics, Heidelberg University,
Im Neuenheimer Feld 227, Heidelberg, Germany

^dInstituto de Física de Cantabria (CSIC-UC),
Av. de los Castros, Santander, Spain

^eEPT, Karlsruhe Institute of Technology,
Wolfgang-Gaede-Str. 1, Karlsruhe, Germany

^fInstitute of High Energy Physics of the Austrian Academy of Sciences,
Dominikanerbastei 16, A-1010, Vienna, Austria

E-mail: oliwia.agnieszka.kaluzinska@cern.ch

ABSTRACT: To face the higher levels of radiation due to the 10-fold increase in integrated luminosity during the High-Luminosity LHC, the CMS detector will replace the current Calorimeter Endcap (CE) using the High-Granularity Calorimeter (HGCal) concept. The electromagnetic section as well as the high-radiation regions of the hadronic section of the CE will be equipped with silicon pad sensors, covering a total area of 620 m². Fluences up to 1.0 · 10¹⁶ n_{eq}/cm² and doses up to 2 MGy are expected considering an integrated luminosity of 3 ab⁻¹. The whole CE will normally operate at -35°C in order to mitigate the effects of radiation damage.

The silicon sensors are processed on 8-inch p-type wafers with an active thickness of 300 μm, 200 μm and 120 μm and cut into hexagonal shapes for optimal use of the wafer area and tiling. With each main sensor several small sized test structures (e.g. pad diodes) are hosted on the wafers, used for quality assurance and radiation hardness tests. In order to investigate the radiation-induced bulk damage, 28 diodes have been irradiated with reactor neutrons at the TRIGA reactor in JSI (Jožef Stefan Institute, Ljubljana) to 13 fluences between 6.5 · 10¹⁴ n_{eq}/cm² and 1.5 · 10¹⁶ n_{eq}/cm².

The charge collection of the irradiated silicon diodes was determined through transient current technique (TCT) measurements. The study focuses on the isothermal annealing behaviour of the

*Corresponding author.

bulk material at 60°C. The results have been used to extend the usage of thicker silicon sensors in regions expecting higher fluences and are being used to estimate the expected annealing effects of the silicon sensors during year-end technical stops and long HL-LHC shutdowns currently foreseen with a temperature around 0°C.

KEYWORDS: Calorimeters; Radiation damage to detector materials (solid state); Radiation-hard detectors; Solid state detectors

ARXIV EPRINT: [2503.01520](https://arxiv.org/abs/2503.01520)

Contents

1	Introduction	1
2	Samples	2
3	Measurement setups	5
4	Data analysis procedure	8
5	Systematic uncertainties	8
6	Results	11
7	Conclusions	14

1 Introduction

The High-Luminosity Large Hadron Collider (HL-LHC) upgrade aims at enhancing the performance of the LHC, the most powerful particle accelerator in the world, to boost the potential for scientific discoveries starting from 2030. The project includes increasing the luminosity (and thus the collision rate) by a factor of five beyond the original LHC design specifications. The integrated luminosity will be increased by a factor of ten. This poses significant challenges in terms of radiation tolerance and event pileup for the detectors [1, 2].

As part of the HL-LHC upgrade within the CMS Experiment [3], the current Calorimeter Endcap (CE) will be replaced, using the novel High Granularity Calorimeter (HGCAL) concept. It will consist of 47 sampling layers interleaved with Cu, CuW, Pb and stainless steel absorber plates and will include nearly six million readout channels.

Silicon sensors were selected as active material for the majority of the CE upgrade due to their compactness, rapid signal formation, and adequate radiation hardness. At the HL-LHC, these silicon sensors will be subjected to hadron fluences ranging from approximately $2.0 \cdot 10^{14}$ 1 MeV neutron-equivalents per square centimeter ($n_{\text{eq}}/\text{cm}^2$) to $1.0 \cdot 10^{16}$ $n_{\text{eq}}/\text{cm}^2$ after an integrated luminosity of 3000 fb^{-1} . Around 620 m^2 of silicon sensors will cover the entire electromagnetic (CE-E) section and the high-radiation region of the hadronic (CE-H) section of the calorimeter. The silicon sensors are fabricated on 8-inch wafers and diced to form a hexagonal shape for efficient use of the wafer area and tiling [2, 4].

In this paper, we report about three measurement campaigns investigating isothermal annealing behaviour of the charge collection capability of the silicon bulk material using dedicated test structure $5 \times 5 \text{ mm}^2$ diodes from the edge region of the wafer, irradiated with reactor neutrons up to $1.5 \cdot 10^{16}$ $n_{\text{eq}}/\text{cm}^2$. As the CMS detector region to be occupied by CE will be irradiated predominantly by neutrons [2], these particles were the main focus of the irradiation. The campaigns were performed in 2021 and 2023 in two different measurement setups. Section 2 gives an overview and detailed information about the samples used in each campaign. The measurement setups and techniques are described in section 3. An insight into the data analysis procedure is presented in section 4. The discussion on the systematic uncertainties is provided in section 5. The results and interpretation of the obtained data can be found in section 6. Finally, the summary and conclusions are given in section 7.

2 Samples

The silicon sensors to be used in the CMS Calorimeter Endcap upgrade consist of DC-coupled, planar, high resistivity ($> 3 \text{ k}\Omega \text{ cm}$), p-type hexagonal silicon sensors with a crystal orientation of $\langle 100 \rangle$ produced on 8-inch circular wafers by Hamamatsu Photonics K.K.¹ (shown in figure 1) [2]. Sensors are produced in three different active thicknesses and two material types. 300 μm and 200 μm sensors are produced in the float zone (FZ) process, whereas 120 μm sensors are produced in the epitaxial (EPI) process on top of a handling wafer of $\sim 180 \mu\text{m}$ thickness. The remaining space left around the hexagonal main sensor (called “halfmoons”) is used for the fabrication of small-sized test structures, which have an identical production process as the main sensor. The test structures include, among others, single diodes which can be used to investigate the bulk radiation hardness of the sensors. For the measurements presented here, square diodes with a side length of 5 mm are chosen. The (main) n+ pad implant is surrounded by a guard ring.

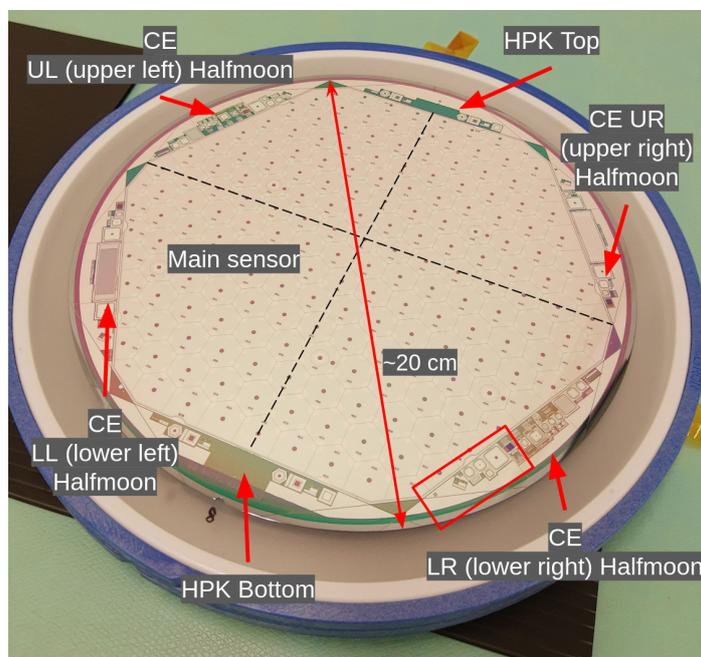


Figure 1. CE full sensor on 8-inch circular wafer including test structures. The silicon crystal orientation is presented as black dashed lines. The example halfmoon section containing the diode used in this publication is marked in red frame (the diode used has cross-sectional area $5 \times 5 \text{ mm}^2$). Reproduced from [14]. CC BY 4.0.

In the following, results from three independent charge collection measurement campaigns are presented, performed in two different setups referred to as “TCT+ setup” and “Particulars setup”.² The setups are described in detail in section 3. In the TCT+ setup, two measurement campaigns were performed — one in 2021 with CE version-1 prototype samples, irradiated to intermediate fluences ($6.5 \cdot 10^{14} \text{ n}_{\text{eq}}/\text{cm}^2$ to $1.0 \cdot 10^{16} \text{ n}_{\text{eq}}/\text{cm}^2$) and one in early 2023 with CE version-2 prototype samples (and with additional version-1 prototype 120 μm samples), irradiated to higher fluences ($1.5 \cdot 10^{15} \text{ n}_{\text{eq}}/\text{cm}^2$ to $1.4 \cdot 10^{16} \text{ n}_{\text{eq}}/\text{cm}^2$). The third campaign was performed in the Particulars

¹<https://www.hamamatsu.com/eu/en.html>

²<https://particulars.si/index.php>

Table 1. Data set overview for 2021 TCT+ campaign. ‘LR’ refers to the lower right halfmoon of the wafer (ref. figure 1). The “Est. initial 60°C offset” is explained in the text. For the last annealing step, the samples were annealed in two batches with different times as indicated in the last row.

Sensor ID	Active thickness [μm]	Fluence [$10^{15} \text{ n}_{\text{eq}}/\text{cm}^2$]	Irradiation time [min]	Est. initial 60°C offset [min]
1002_LR	300	0.65	7.4	$1.0 \pm_{0.4}^{0.8}$
1003_LR	300	1.00	10.8	$1.6 \pm_{0.7}^{1.3}$
1102_LR	300	1.50	16.2	$2.4 \pm_{1.1}^{2.1}$
2002_LR	200	1.00	10.8	$1.6 \pm_{0.7}^{1.3}$
2003_LR	200	1.50	16.2	$2.4 \pm_{1.1}^{2.1}$
2102_LR	200	2.50	27.5	$4.0 \pm_{1.9}^{3.8}$
3008_LR	120	1.50	16.2	$2.4 \pm_{1.1}^{2.1}$
3007_LR	120	2.50	27.5	$4.0 \pm_{1.9}^{3.8}$
3003_LR	120	10.00	108.0	$14.2 \pm_{7.2}^{14.2}$
Annealing steps [min]	10, 30, 90, 120 (all samples), 260 (1003_LR, 2002_LR, 3008_LR, 3003_LR), 300 (1002_LR, 1102_LR, 2003_LR, 2102_LR, 3007_LR)			

setup in 2023, where CE pre-series samples, irradiated to higher fluences ($2.0 \cdot 10^{15} \text{ n}_{\text{eq}}/\text{cm}^2$ to $1.5 \cdot 10^{16} \text{ n}_{\text{eq}}/\text{cm}^2$), were measured. Version-1, version-2 prototypes and pre-series sensors represent different iterations of sensors produced for CE. The iterations differ in terms of parameters regarding the oxidation process. Typical variations in the bulk production process are known to be independent of the manufacturer’s oxidation process. The bulk properties of the silicon material are defined in the procurement documents and are expected to remain consistent across production iterations. All wafers are certified to meet the technical requirements specified in the invitation to tender. As a result, consistent performance in charge collection measurements is expected. In each campaign, isothermal annealing studies were performed. Detailed information about the samples and annealing steps for each campaign is listed in tables 1, 2 and 3, respectively. All diodes were irradiated with reactor neutrons at the Training, Research, Isotopes, General Atomics (TRIGA) reactor located at the Jožef Stefan Institute, Ljubljana, Slovenia [5, 6]. In addition to the irradiated samples, each campaign included a set of unirradiated samples — one for each thickness — to serve as reference sensors.

During the irradiation, the diodes are exposed to temperatures reaching up to $(50 \pm 5)^\circ\text{C}$. This is taken into account as in-reactor annealing when determining the total annealing times and its uncertainty for the individual samples. The temperature during the irradiation is assumed to increase from the minimum of 25°C to the maximum temperature after 30 min where it stabilizes. The temperature increase and decrease is assumed to follow exponential functions, with distinct time constants for each phase [7]. Based on this information, the irradiation time, listed in tables 1, 2 and 3, is converted to an equivalent annealing time at 60°C (using the so-called Hamburg model derived in [8, 9]) and included in the tables as *estimated initial 60°C offset*. The estimated annealing times have an uncertainty which is displayed in the figures as error bars.

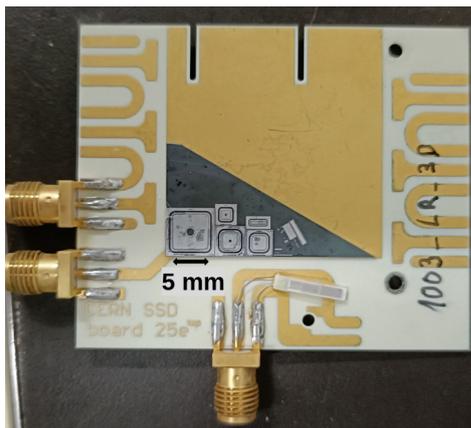
Table 2. Data set overview for 2023 TCT+ campaign. ‘LR’ refers to the lower right, ‘LL’ refers to the lower left, ‘UR’ to upper right and ‘UL’ to upper left halfmoon of the wafer (ref. figure 1). The “Est. initial 60°C offset” is explained in the text.

Sensor ID	Active thickness [$\mu\text{ m}$]	Fluence [$10^{15}\text{ n}_{\text{eq}}/\text{cm}^2$]	Irradiation time [min]	Est. initial 60°C offset [min]
N4791_11_LR	300	1.50	16.2	$2.4 \pm_{1.1}^{2.1}$
N4791_12_LL	300	2.00	21.8	$3.2 \pm_{1.5}^{3.0}$
N4791_13_LR	300	3.00	32.6	$4.7 \pm_{2.3}^{4.4}$
N4792_10_LR	200	2.00	21.8	$3.2 \pm_{1.5}^{3.0}$
N4792_11_LL	200	4.00	43.3	$6.1 \pm_{3.0}^{5.9}$
N4792_12_LR	200	5.50	59.5	$8.1 \pm_{4.1}^{8.0}$
N4789_10_LL	120	5.50	59.5	$8.1 \pm_{4.1}^{8.0}$
N4789_12_UL	120	10.00	108.0	$14.2 \pm_{7.2}^{14.2}$
N4789_13_UR	120	14.00	151.2	$19.6 \pm_{10.0}^{19.8}$
3008_UR	120	1.50	16.2	$2.4 \pm_{1.1}^{2.1}$
3007_UR	120	2.50	27.5	$4.0 \pm_{1.9}^{3.8}$
3003_UR	120	10.00	108.0	$14.2 \pm_{7.2}^{14.2}$
Annealing steps [min]	0, 90			

Table 3. Data set overview for 2023 Particulars campaign. ‘LR’ refers to the lower right, ‘LL’ refers to the lower left and ‘UL’ to upper left halfmoon of the wafer (ref. figure 1). The “Est. initial 60°C offset” is explained in the text.

Sensor ID	Active thickness [$\mu\text{ m}$]	Fluence [$10^{15}\text{ n}_{\text{eq}}/\text{cm}^2$]	Irradiation time [min]	Est. initial 60°C offset [min]
N8738_1 LL1	300	2.00	21.8	$3.2 \pm_{1.5}^{3.0}$
N8738_2 LR	300	4.00	43.3	$6.1 \pm_{3.0}^{5.9}$
N8740_1 LL1	200	4.00	43.3	$6.1 \pm_{3.0}^{5.9}$
N8740_2 LL1	200	6.00	64.9	$8.8 \pm_{4.4}^{8.7}$
N8740_3 LL2	200	8.00	86.8	$11.6 \pm_{5.8}^{11.5}$
N8737_3 UL	120	6.00	64.9	$8.8 \pm_{4.4}^{8.7}$
N8737_2 LL1	120	15.00	161.8	$20.9 \pm_{10.7}^{21.1}$
Annealing steps [min]	0, 30, 55, 85, 120, 150, 200, 275, 365, 510			

After the irradiation, samples are mounted on a printed circuit board (PCB) with a conductive silver paint. The diode is wire-bonded to a SubMiniature version A (SMA) connector enabling appropriate connection to the measurement circuit through which it is grounded. The diode’s guard ring is grounded. The high voltage is applied to the diode’s backside. In order to control the temperature of the diode, in the following referred to as device under test (DUT), during the measurement, a PT1000



(a) Large custom designed 2-layer PCB layout. The full test structure part fits onto the PCB.



(b) Small custom designed 2-layer PCB layout. The test structure sample has to be diced to fit onto the PCB.

Figure 2. Two PCB layouts used in this study.

resistor is glued close to the DUT. In the 2021 TCT+ campaign, a large custom designed 2-layer PCB layout, shown in figure 2(a), was used. Such a large PCB size was used to avoid further dicing the pre-diced triangles of the halfmoons containing the DUT. However, it was found non-optimal for homogeneous cooling of the PCB area, especially below the DUT, as it is not fully covered by the copper holder that provides cooling (ref. section 3). For the next two campaigns (both 2023 campaigns), a small custom designed 2-layer PCB layout (shown in figure 2(b)) was chosen to improve homogeneity of the temperature across the DUT. To fit the size of smaller PCB, the silicon triangles had to be diced, which was performed using a diamond scribe. This posed the risk of damaging the diode intended for testing during the cutting especially since the silicon crystal orientation and the diode orientation differ by 30 degrees, as can be seen in figure 1.

Between measurements, the samples were annealed in several steps. The annealing is performed in a dedicated oven preheated to a temperature of 60°C. For the annealing, the samples are placed on preheated copper blocks for good thermal contact. The temperature was monitored every second using the PT1000 resistor attached to one of the PCBs. The estimated initial offset is added to the additional annealing steps resulting in the total annealing time. The additional annealing steps are listed in the bottom row of tables 1, 2 and 3. Except during annealing or measurement, the samples are stored in a freezer (at -20°C) to avoid excess annealing.

3 Measurement setups

The ability to collect charge by the silicon sensors can be measured using the Transient Current Technique (TCT) [10]. In this method, a laser pulse generates electron-hole pairs in the silicon bulk. As an external bias voltage is applied to the silicon diode, the generated charge carriers drift towards the corresponding electrodes. The induced current is recorded as a function of time.

For all measurements reported in this publication, the DUTs are reverse-biased, with high voltage applied to the backside bulk contact. The readout is connected to an oscilloscope via an amplifier. Laser light of 1064 nm wavelength (infra-red) is injected from the front side of the DUT (IR-top TCT) directed at a 1 mm-diameter hole in center of the metal contact layer of the diode. In the TCT+ setup,

the laser had a repetition frequency of 200 Hz and a duty cycle of 0.002%, while in the Particulars setup, the repetition frequency was 1 kHz with a duty cycle of 0.0002%. The schematic principle of the top-TCT measurement is shown in figure 3. In the 2021 TCT+ campaign, a laser intensity equivalent to 33 MIPs (minimum ionizing particles) was used. For the 2023 TCT+ and Particulars campaigns, the intensity was increased to 40 MIPs, which is still within the linear region of the laser intensity where plasma effects are no concern, in order to enhance the signal for highly irradiated diodes [11]. In the previous studies ([2, 12]), it was shown that an IR light signal equivalent to multiple MIPs gives comparable results to single-MIP measurements using a radioactive source. The measurements are conducted with the DUT cooled down to -20°C in order to reduce the leakage current of the irradiated sensors. The difference of the measurement temperature compared to the -35°C operation temperature has no influence on the charge collection efficiency (CCE, ref. section 6), while for the leakage current the current can easily be scaled. This was cross-checked by measurements using modules assembled with irradiated sensors.

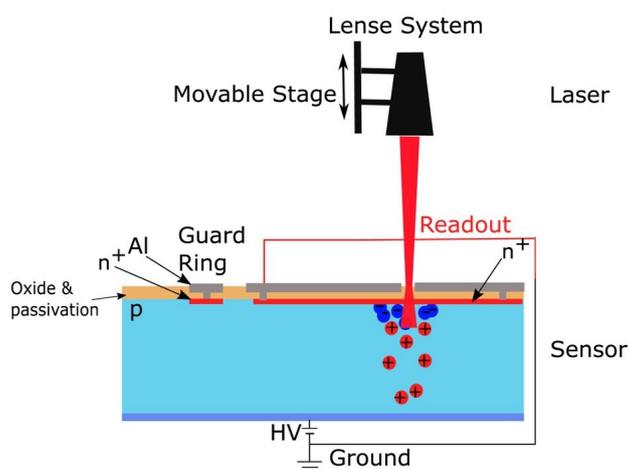


Figure 3. Schematic view of top-TCT measurement. Reproduced with permission from [13].

The diagram of the TCT+ setup is shown in figure 4. The schematic of the Particulars setup is equivalent to the TCT+ setup schematic, shown in figure 4, except for the brand of the amplifier, laser head, laser driver and pulse generator (all supplied by Particulars). Both setups offer the capability for electrical characterization in addition to TCT measurements, enabling the measurement of leakage current (IV) and capacitance (CV) as a function of the reverse bias voltage. More details about IV and CV measurements can be found in [7].

Both the TCT+ (figure 5(a)) and Particulars (figure 5(b)) setups serve the same purpose but differ in specific details. In both setups, the DUT is mounted on a PCB attached to a copper holder or plate, which is cooled by Peltier elements connected to a chiller. To control positioning, the TCT+ setup uses an XYZ stage that moves the copper holder while keeping the laser fixed, whereas the Particulars setup uses an XY stage to position the DUT and a separate Z stage to adjust the laser focus. Both setups use a broadband current amplifier, the TCT+ setup is equipped with a CIVIDEC C2-HV amplifier with 43 dB gain and integrated bias-T [15], while the Particulars setup uses a wide-band amplifier with 53 dB gain by Particulars company [16]. A switch box in both setups allows for changing between TCT, IV, and CV measurements. To prevent condensation and frost, both setups are housed in a Faraday cage (as shown in figure 5) and flushed with dry air. Additionally, in the Particulars setup, dry

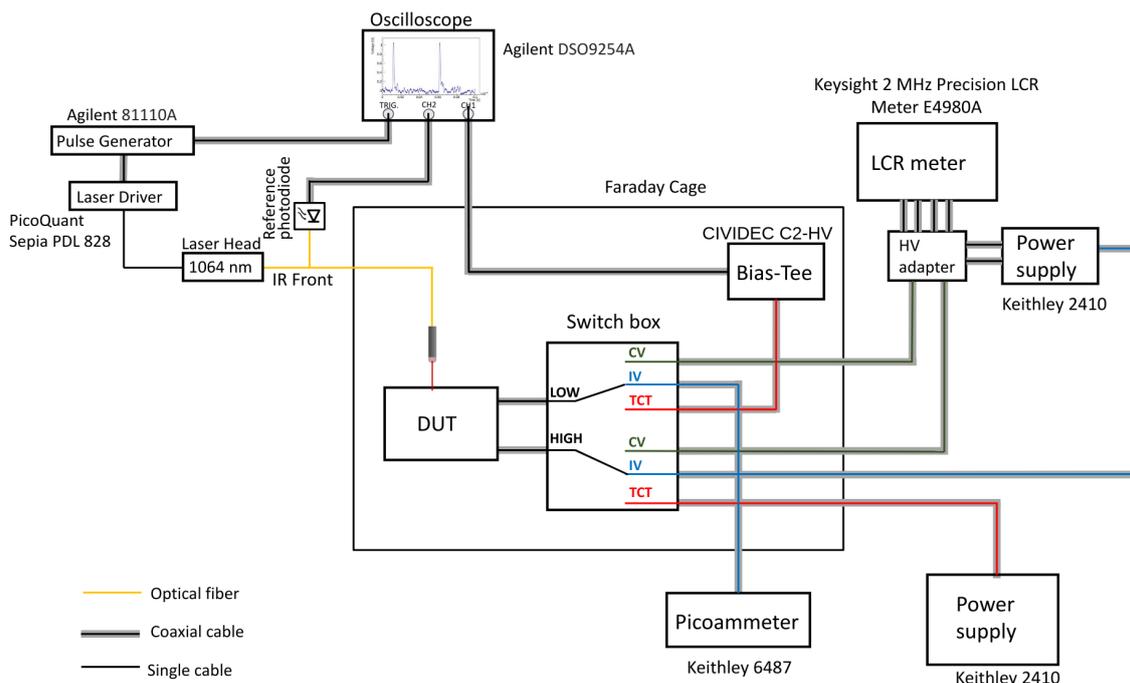
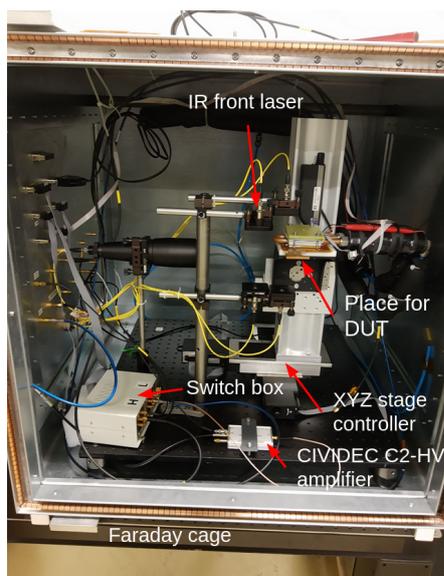
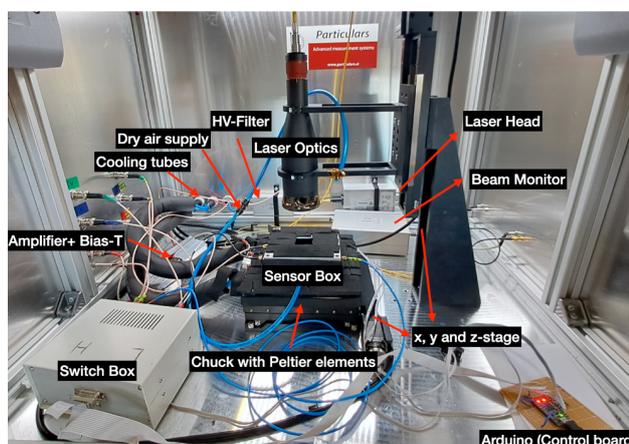


Figure 4. Diagram of the TCT+ setup. Reproduced from [14]. CC BY 4.0.



(a) TCT+ setup.



(b) Particulars setup.

Figure 5. Two TCT setups used to obtain data reported in this publication, not showing the instruments located outside of the Faraday cages. Reproduced from [14]. CC BY 4.0.

air also flushes the enclosed sensor box, where the DUT is placed. Both setups include a reference photodiode to monitor laser intensity, but the beam splitting differs. In the TCT+ setup, a fused coupler from OZ Optics [17] splits the beam, directing 90% of the laser light to the DUT and 10% to the photodiode. In the Particulars setup, a fiber splitter inside the beam monitor evenly divides the intensity, sending 50% to the DUT and 50% to the photodiode. The Particulars setup also contains

an HV filter (to remove the noise in the HV line coming from the power supply) and external bias-T, which are both in the TCT+ circuit between the power supply and the sensor. These setups have been cross checked in a dedicated study to assure there is no influence in the results.

4 Data analysis procedure

Charge collection. To determine the charge collection value, the TCT signal is measured and recorded. The transient current pulse is converted into a voltage pulse through the input impedance ($50\ \Omega$) of the amplifier. Figure 6(a) displays an example of TCT signals obtained during the 2023 TCT+ campaign from an irradiated diode at various bias voltages up to the chosen measurement limit of 900 V. This limit was chosen to reduce the risk of destructive discharges. The signal's amplitude correlates with the applied bias voltage on the DUT. In both TCT+ setup campaigns, the waveform measured at a particular bias voltage utilizes an average waveform, representing an average of 1000 samples. For the 2023 Particulars campaign, 300 averaged waveforms are captured for each bias voltage, with a single waveform averaging 50 samples.

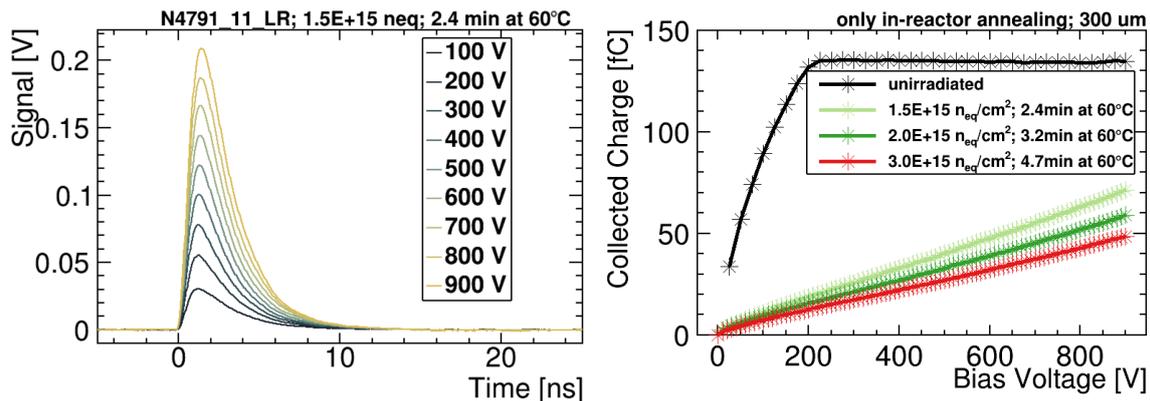
The collected charge value at a specific bias voltage is calculated as the integral of the TCT signal waveform (for a time window at 0 ns to 25 ns) divided by the amplifier gain. An example of the charge collection vs. bias voltage dependence is depicted in figure 6(b). After this initial data processing, the charge collection at a selected bias voltage can be analysed as a function of fluence or annealing time.

Saturation voltage. In figure 6(b), it is clearly visible that the charge collection for unirradiated samples saturates after reaching a certain voltage. This point is identified as the depletion voltage, which is determined using the same method as in CV measurements [7]: by fitting the rising part of the curve and the plateau region, with the intersection point of these two fits representing the extracted depletion voltage. However, for irradiated sensors, extracting the depletion voltage is significantly more challenging. It is noteworthy that we distinguish “saturation voltage” from “depletion voltage”. This distinction arises from our observation that the concept of depletion voltage no longer holds true for irradiated devices since for example its value depends on the measurement frequency (for CV measurement) and the temperature [7, 18].

For the low fluences and thin sensors covered in this study, where the saturation voltage lies well below the measurement limit of 900 V, extracting the saturation voltage from charge collection measurements becomes feasible. An example is shown in figure 7. Unlike in unirradiated sensors, the plateau region is absent in irradiated sensors due to the occurrence of charge trapping, of which the probability decreases with increasing bias voltage and hence the electric field. This results in an increase in charge beyond the saturation voltage. Each part is fitted with a linear fit, and the intersection point is identified as the extracted saturation voltage. If the second rising part is not reached within the measured voltage range or the two slopes become too similar, it becomes impossible to extract the saturation voltage (as visible in figure 6(b) for the irradiated samples).

5 Systematic uncertainties

Systematic uncertainties of charge collection were determined by considering five factors present during the campaigns, with two being the most significant: the laser and the temperature stability. Long-term measurements of the laser intensity in the TCT+ setup, recorded by the reference photodiode (ref. section 3), revealed a small instability over time. To account for this instability, all the charge



(a) TCT waveforms for various bias voltages. (b) Integral of TCT waveforms building the collected charge value versus bias voltage.

Figure 6. Example of data processing on 300 μm samples without additional annealing, obtained during the 2023 TCT+ campaign.

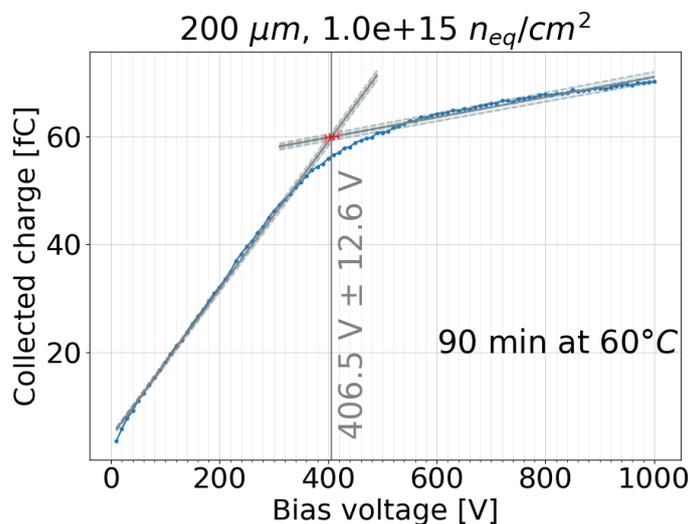


Figure 7. Saturation voltage extraction example. Data obtained during 2021 TCT+ campaign.

collection results obtained using the TCT+ setup in this study were calibrated by normalizing the laser intensity during the measurements to the intensity recorded during a reference measurement with a 300 μm unirradiated diode. After normalization, the uncertainty of the laser intensity was determined to be 2.5%

Next, we considered the uncertainty in the sensor's temperature during measurements, which affects the laser absorption in the silicon. In the TCT+ setup, the temperature is controlled within $\pm 1^\circ\text{C}$. The 25e PCB layout used for the 2021 TCT+ campaign was larger than the copper holder in the setup, leading to temperature inhomogeneity across the PCB. Additionally, it was observed, using an infra-red camera, that the SMA connectors were warmer than the middle part of the PCB, where the diode was located. The PT1000 sensor's proximity to the SMA connector (ref. figure 2(a)) resulted in higher temperature readings. This suggests that the diode could have been cooled more than intended.

The laser band-to-band absorption (α_{bb}) depends on the temperature as follows [19–21]:

$$\alpha_{\text{bb}}(T) = \alpha_{\text{bb}}(T_0)(T/T_0)^{c_T T_0} \quad (5.1)$$

where T_0 is the nominal temperature for the absorption coefficient data (-20°C in our case) and c_T is the temperature coefficient. Based on the infra-red images of the setup, we assume that the sensor temperature during the 2021 TCT+ campaign was between -24°C and -19°C . This translates to an uncertainty in charge collection due to temperature change of $+6.1\%$ and -1.6% . For the 2023 TCT+ campaign, smaller PCBs were used (ref. figure 2(b)) which allows improved temperature control. However, since the DUTs were already mounted to the PCBs, it was not possible to place a PT1000 directly onto them without the risk of damaging the DUT in the soldering process and uncontrolled annealing. Instead, a PT1000 was attached to an additional copper sheet placed between the copper holder of the TCT+ setup and the PCB. This increased the uncertainty of the temperature measurement. For this campaign, the DUT's temperature is assumed to be between -25°C and -18°C , resulting in an uncertainty in the charge collection due to temperature of $+7.6\%$ and -3.2% .

There are two additional charge collection uncertainties considered for the TCT+ setup. As the setup, during both campaigns, was also used by other users, it required everyday calibration. This was achieved by measuring the charge collection of the $300\ \mu\text{m}$ unirradiated diode (as a reference) at $+20^\circ\text{C}$ at the start of each measurement day and adjusting the laser intensity to maintain consistent charge collection values. The variations in charge collection resulted in a $\pm 0.8\%$ uncertainty for 2021 TCT+ campaign and a $\pm 0.6\%$ uncertainty for 2023 TCT+ campaign. The second factor concerns the replacement of the amplifier in the setup on one occasion during 2021 TCT+ campaign and two occasions during 2023 TCT+ campaign, due to their failures. After each amplifier replacement, the laser intensity was recalibrated in the same manner as during the daily calibration. The variations in charge collection resulting from the amplifier replacements introduced a $\pm 0.2\%$ uncertainty for 2021 TCT+ campaign and $\pm 0.6\%$ uncertainty for 2023 TCT+ campaign.

All uncertainties were combined in quadrature, resulting in a total charge collection uncertainty of $+6.6\%$ and -3.1% for the 2021 TCT+ campaign and $+8.0\%$ and -4.2% for the 2023 TCT+ campaign.

The Particulars setup was exclusively used for this measurement campaign, with no other users. A temperature uncertainty of the smaller PCB has to be considered ($\pm 1^\circ\text{C}$), as well as an uncertainty regarding the laser stability. To monitor this, a reference $300\ \mu\text{m}$ unirradiated diode was measured daily. It was found that environmental temperature variations slightly influence the laser intensity, as well as the temperature stability of the DUT. To account for this, an overall charge collection uncertainty of 5.0% was estimated.

The last contributing factor to the charge collection uncertainty is the fit uncertainty of the Gaussian fit applied to the charge collection distribution of the 300 recorded events, of which the most probable value defines the CC for each measured voltage.

To assess the uncertainty in annealing time, we first account for the uncertainty in the estimated initial 60°C offset, which is derived by considering the full impact of the reactor temperature uncertainty. These effects become more pronounced for longer irradiation times. Considering the high total annealing times — up to 500 min — done in this campaign, the impact of the uncertainty of the in-reactor annealing becomes negligible. Next, for the individual annealing steps, we assign a 0.5°C uncertainty in the temperature measurement, based on the temperature recordings. The primary factor contributing to this uncertainty is the potential for small temperature variations between different

Table 4. Summary of all systematic uncertainties in charge collection, *: for 2023 Particulars campaign the laser instability and everyday variations in CC are not distinguishable.

	2021 TCT+	2023 TCT+	2023 Particulars
Laser instability	$\pm 2.5\%$	$\pm 2.5\%$	*
Laser absorption (due to temperature)	$+6.1\% -1.6\%$	$+7.6\% -3.2\%$	$\pm 1.6\%$
Everyday variations in charge collection	$\pm 0.8\%$	$\pm 0.6\%$	*
Amplifier replacement	$\pm 0.2\%$	$\pm 0.6\%$	–
Total	$+6.6\% - 3.1\%$	$+8.0\% -4.2\%$	$\pm 5.0\%$

PCBs. The uncertainty in the time measurement itself is negligible. All systematic uncertainties in charge collection are summarized in the table 4.

The uncertainty on the fluence is consistently 10% for all fluences.

In this paper, the extracted saturation voltages from both CV and CC measurements are presented as well. The uncertainty associated with the saturation voltage from CV measurements is well described in [7]. To estimate the uncertainty on the extracted saturation voltage from CC measurements, an automated fitting method was employed (as shown in figure 7). Since the data around the crossing point is not strictly linear, the uncertainty was assessed by varying the fit range by $\pm 10\%$, repeating the fit, and re-evaluating the crossing point. The maximum upward and downward deviations from the original value were taken as the uncertainty range. The contribution from the linear fit itself is negligible and thus not considered separately [22].

6 Results

The data collected throughout different campaigns are used to characterize the behaviour of the charge collection (ref. section 4) over a broad range of fluence. Since only an IR laser is used, the individual trapping times are not accessible within this measurement campaign [23–25]. Figure 8 depicts the charge collection behaviour at a bias voltage of 600 V after approximately 90-minute annealing at 60°C for all three sensor thicknesses. In figure 8(a), only the charge collection data from the two 2023 campaigns are presented, while the results from the 2021 TCT+ campaign are omitted since a different laser intensity was used (33 MIPs equivalent vs. 40). The horizontal dotted lines represent the charge collection values for unirradiated sensors, with red indicating 300 μm , blue representing 200 μm , and black representing 120 μm . Comparing the absolute collected charge, at high fluences like $2.0 \cdot 10^{15} \text{ n}_{\text{eq}}/\text{cm}^2$ or $4.0 \cdot 10^{15} \text{ n}_{\text{eq}}/\text{cm}^2$, the 200 μm sensors still collect roughly 20% more charge than the 300 μm sensors. This can be attributed to the fact that a larger region of the detector is depleted for thinner sensors at a fixed voltage. A decrease in collected charge with increasing fluence is consistently observed across all three thicknesses. The collected charge values demonstrate agreement within the margins of uncertainty across different campaigns.

In figure 8(b), the charge collection efficiency (CCE), defined as the charge collection of an irradiated sample divided by the charge collection of the non-irradiated reference sensors, across all three campaigns is depicted. A red horizontal line at 50% CCE is included for better visibility, providing an additional safety margin in case the LHC delivers a higher integrated luminosity. Once more, a distinct trend emerges: as fluence increases, the charge collection efficiency decreases across all

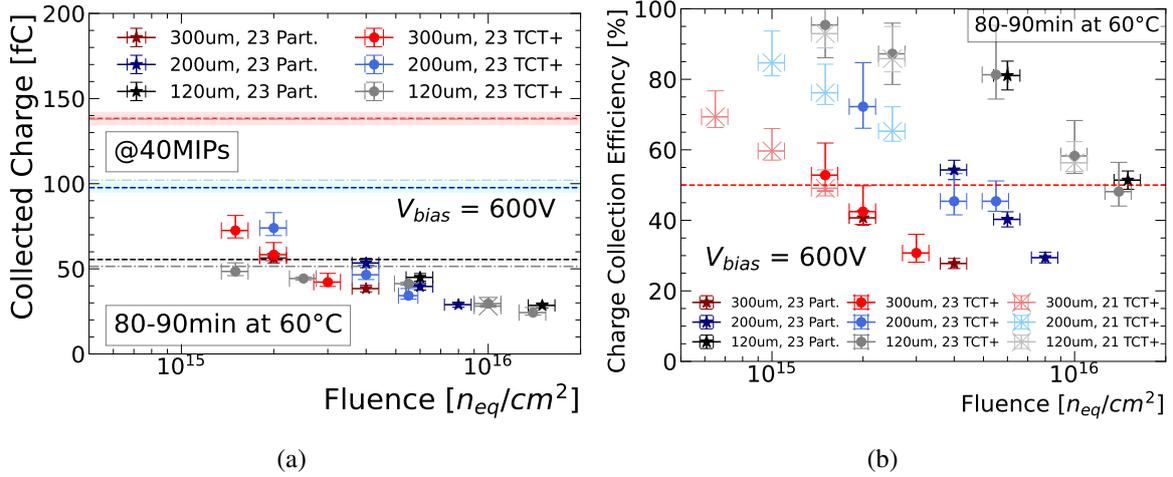


Figure 8. Charge collection (a) and charge collection efficiency (b) as function of the fluence for all the campaigns.

thickness variations. The data collected from different campaigns and studies on earlier CE prototypes at lower fluences [26] exhibit agreement within the margins of uncertainty. At a fixed fluence, the 300 μm samples demonstrate the lowest charge collection efficiency, whereas the 120 μm samples exhibit the highest. Since thinner sensors collect more charge (as shown in figure 8(a)), especially at higher fluences, this indicates superior radiation hardness, making them suitable for deployment in the highest radiation regions of the CE. Specifically, for 600 V, the 300 μm samples achieve 50% charge collection efficiency up to a fluence of $1.5 \cdot 10^{15} \text{ n}_{\text{eq}}/\text{cm}^2$, the 200 μm samples up to $4.0 \cdot 10^{15} \text{ n}_{\text{eq}}/\text{cm}^2$, and the 120 μm samples up to $1.5 \cdot 10^{16} \text{ n}_{\text{eq}}/\text{cm}^2$. At a fluence of $1.5 \cdot 10^{15} \text{ n}_{\text{eq}}/\text{cm}^2$, the 120 μm samples still have nearly 100% efficiency.

For the samples irradiated in 2023 and characterized in the Particulars setup the annealing was performed in 9 steps with TCT measurements between the steps. A visible increase in collected charge (figure 9(a)) and efficiency of collected charge (figure 9(b)) is observed for all samples during the

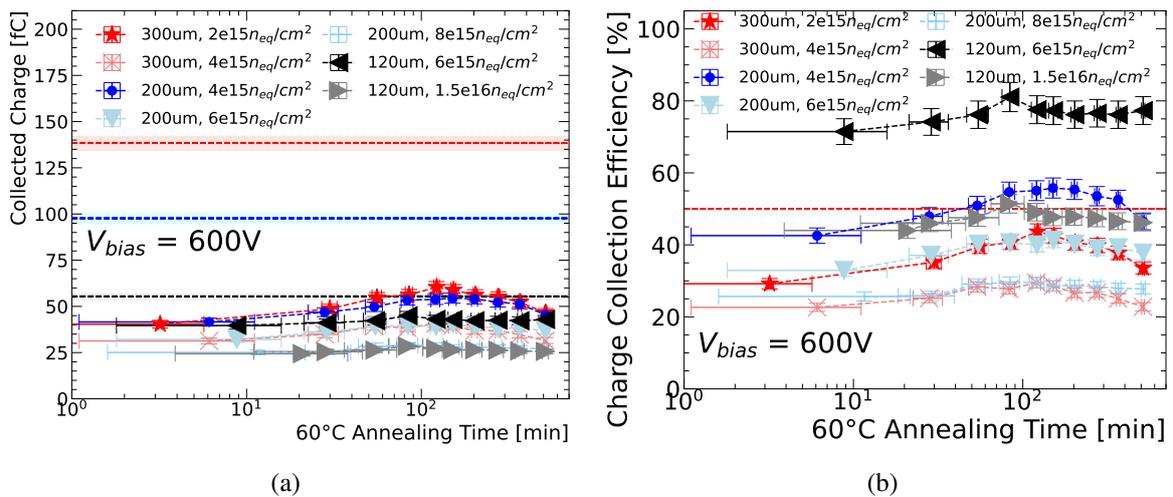


Figure 9. Charge collection (a) and charge collection efficiency (b) as function of the annealing time. Data collected during 2023 Particulars campaign.

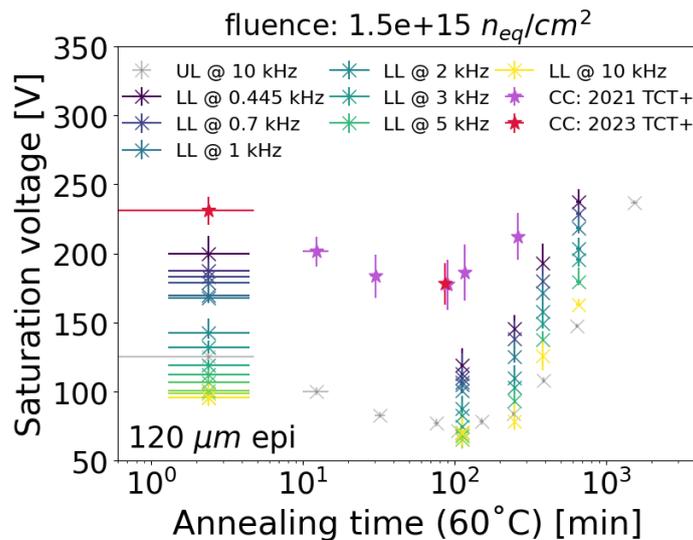


Figure 10. Saturation (former “depletion”) voltage as a function of the annealing time at 60°C. Data extracted from CV measurements at several frequencies presented in [7] and charge collection (CC) measurements (2021 TCT+ and 2023 TCT+ campaigns). ‘UL’ refers to the upper left, and ‘LL’ to the lower left halfmoon of the wafer (ref. figure 1).

time range dominated by beneficial annealing. The maximum is reached at around 120 minutes for float zone samples and around 85 minutes for epitaxial samples followed by a decrease into time range dominated by reverse annealing. These findings align with previous CC studies on p-type sensors [27–29] and with the corresponding findings from CV measurements [7].

In previous studies [7], the saturation voltage (ref. section 4) extracted from capacitance versus bias voltage (CV) measurements, as a function of annealing time, was used as an input to the Hamburg model. However, the saturation voltage obtained from CV measurements is well known for its sensitivity to both frequency and temperature [7, 18]. To mitigate this dependency, we use charge collection as an independent alternative approach. Figure 10 illustrates a comparison between the depletion voltage extracted from CV and charge collection measurements, using a sample with the following properties: 120 µm thickness, irradiated to $1.5 \cdot 10^{15} \text{ neq/cm}^2$. The charge collection was measured during two campaigns: 2021 TCT+ and 2023 TCT+.

It is evident that, as the CV frequency increases, the extracted saturation voltage decreases. Moreover, the saturation voltage extracted from charge collection is higher compared to those obtained from CV with the lowest CV frequency being 445 Hz. This poses a challenge for thicker and highly irradiated sensors, where the saturation voltage is correspondingly higher. If the saturation voltage exceeds 900 V, it becomes infeasible to measure it using the CC method with the mentioned bias voltage limitation, whereas for CV it can be tuned down by frequency choice. However, the extraction of the saturation voltage using the CC method is unaffected by measurement settings such as the frequency choice of the CV measurement, which is considered an advantage. For the temperature range -30°C to -15°C the saturation voltage for irradiated samples extracted from charge collection measurements stays constant within 0.1%, which was tested with dedicated measurement within this study (figure not shown).

The extraction of the saturation voltage from charge collection data is feasible for samples where the saturation voltage is well below the measurement limit of 900 V which is true for the thin samples

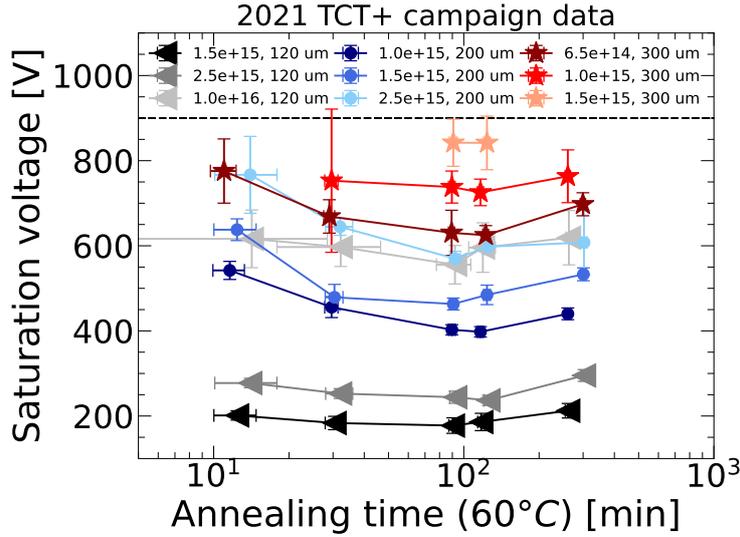


Figure 11. Saturation voltage as a function of annealing time at 60°C extracted from charge collection data from TCT 2021+ campaign. The measurement limit of 900 V is highlighted as black dashed line.

and low fluence 300 μm sample for the TCT+ 2021 campaign. The extracted saturation voltages are presented in figure 11. The minimum saturation voltage is observed between 90 and 120 minutes, consistent with results from CV measurements [7]. The results obtained from the measurement of the saturation voltage as well as the leakage current for different annealing temperatures can be used as inputs to the Hamburg model [9]. Even though not all assumptions are made for that model, it still provides a good parameterization to extract the temperature dependence and helps to predict the evolution of parameters like charge collection efficiency and leakage current — which is important for noise and power consumption — throughout the operational lifetime of the experiment. However, such study benefits from extending the annealing campaign results to fluences which are lower than the ones reported here. Hence it is beyond the scope of this paper, but considered in ongoing campaigns.

7 Conclusions

The charge collection annealing behaviour of silicon diodes from 8-inch p-type wafers from the CE prototypes and pre-series phases is presented for a large fluence range. The combined results from three charge collection campaigns confirm that the bulk material of diodes from different CE manufacturing phases exhibit consistent behaviour, as expected.

The results obtained in this study provide valuable input for optimizing the CE detector layout. Although thick sensors have a lower radiation tolerance, they benefit from an initially higher signal-to-noise ratio. Based on the results hereby reported, on the measurements of the leakage current [7] and on the expected performance of the front-end readout chip to be used in CE [30], one can extend the usage of thicker sensors in higher fluence regions. The previously envisaged layout for the CE planned to use 300 μm sensors only in regions that do not expect higher fluences than $5.0 \cdot 10^{14} \text{ n}_{\text{eq}}/\text{cm}^2$ after an integrated luminosity of 3.0 ab^{-1} , while the 200 μm sensors were planned to cover regions expecting fluences up to $2.5 \cdot 10^{15} \text{ n}_{\text{eq}}/\text{cm}^2$ [2]. After this dedicated high-fluence study, the layout was optimized to use 300 μm sensors in regions up to $1.7 \cdot 10^{15} \text{ n}_{\text{eq}}/\text{cm}^2$ and 200 μm sensors up to

$5.0 \cdot 10^{15} n_{\text{eq}}/\text{cm}^2$, so that the detector benefits from initially larger signals and lower production costs. These fluence limits were calculated based on the measured performance taking a safety margin regarding maximum integrated luminosity into account.

The cooling scheme used for the CE will ensure the operation at -35°C and never exceeding (-30°C). During the shutdown periods it will be warmed up to approximately 0°C . This study supports the case that the silicon sensors will not enter the reverse annealing-dominated region significantly during the CE's operational lifetime. Our findings show that the charge collection behaviour of the novel 8-inch material with respect to annealing time reaches its maximum increase at around 120 minutes for float zone material and around 85 minutes for epitaxial material at 60°C . These results align with CV measurements and other studies on 6-inch p-type sensors, indicating that the CE 8-inch sensors will not experience significant reverse annealing until the end of HL-LHC if we apply the Hamburg model with the parameters values derived in [8], which allows extrapolating down to low temperature.

The presented dataset is currently being extended with an annealing campaign at additional temperatures (6.5°C , 20°C , 30°C , 40°C , 60°C) and annealing steps. Due to the slower annealing at the lower temperatures, results are expected in 2025/2026. This approach will enable to perform the Hamburg model fit at various annealing temperatures and extract for the first time scaling factors between the extracted parameters for the 8-inch p-type material. Consequently, it will be possible to predict the annealing behaviour of CE silicon sensors at 0°C with better accuracy as using the current Hamburg model and establish appropriate CE operating conditions.

Acknowledgments

We thank the CERN EP-DT SSD group for providing the TCT+ setup to perform the measurements described in this publication, in particular Michael Moll, Ruddy Costanzi, Esteban Curras Rivera, Marcos Fernandez Garcia and Moritz Wiehe. Furthermore we thank Dana Groner and Ruddy Costanzi for help in upgrading the Particulars setup. Moreover, we thank the CERN EP R&D programme for funding the Particulars setup and the CMS HGAL silicon sensor group for the discussions about the results presented here. We are grateful for feedback and suggestions from Rachel Yohay, Ronald Lipton, Pedro Silva and Philippe Bloch. This work has been sponsored by the Wolfgang Gentner Programme of the German Federal Ministry of Education and Research (grant no. 13E18CHA). This work has been supported by the Alexander-von-Humboldt-Stiftung.

References

- [1] G. Apollinari et al., *High-Luminosity Large Hadron Collider (HL-LHC): Technical Design Report V. 0.1*, in *CERN Yellow Reports: Monographs* **4**, CERN, Geneva, Switzerland (2017) [[DOI:10.23731/CYRM-2017-004](https://doi.org/10.23731/CYRM-2017-004)] [[CERN-2017-007-M](https://cds.cern.ch/record/2613213)].
- [2] CMS collaboration, *The Phase-2 Upgrade of the CMS Endcap Calorimeter*, [CERN-LHCC-2017-023](https://cds.cern.ch/record/2613213) (2017) [[CMS-TDR-019](https://cds.cern.ch/record/2613213)].
- [3] CMS collaboration, *The CMS Experiment at the CERN LHC*, [2008 JINST 3 S08004](https://cds.cern.ch/record/2613213).
- [4] CMS HGAL collaboration, *Neutron irradiation and electrical characterisation of the first 8'' silicon pad sensor prototypes for the CMS calorimeter endcap upgrade*, [2023 JINST 18 P08024](https://cds.cern.ch/record/2613213) [[arXiv:2209.10159](https://arxiv.org/abs/2209.10159)].

- [5] L. Snoj, G. Žerovnik and A. Trkov, *Computational analysis of irradiation facilities at the JSI TRIGA reactor*, *Appl. Radiat. Isot.* **70** (2012) 483.
- [6] K. Ambrožič, G. Žerovnik and L. Snoj, *Computational analysis of the dose rates at JSI TRIGA reactor irradiation facilities*, *Appl. Radiat. Isot.* **130** (2017) 140.
- [7] J. Kieseler et al., *Isothermal annealing of radiation defects in silicon bulk material of diodes from 8'' silicon wafers*, *2023 JINST* **18** P09010 [arXiv:2211.04849].
- [8] M. Moll, *Radiation damage in silicon particle detectors: Microscopic defects and macroscopic properties*, Ph.D. Thesis, University of Hamburg, Hamburg, Germany (1999) [DOI:10.3204/PUBDB-2016-02525] [DESY-THESIS-1999-040].
- [9] M. Moll, *Displacement damage in silicon detectors for high energy physics*, *IEEE Trans. Nucl. Sci.* **65** (2018) 1561.
- [10] G. Kramberger, *Signal development in irradiated silicon detectors*, Ph.D. Thesis, University of Ljubljana, Ljubljana, Slovenia (2001) [CERN-THESIS-2001-038].
- [11] F.R. Palomo et al., *Plasma effects in silicon detectors and the Two Photon Absorption Transient Current Technique*, in the proceedings of the *2021 21th European Conference on Radiation and Its Effects on Components and Systems (RADECS)*, Vienna, Austria, 13–17 September 2021, pp. 1–6 [DOI:10.1109/radecs53308.2021.9954488].
- [12] E.C. Rivera, *Advanced silicon sensors for future collider experiments*, Ph.D. Thesis, University of Cantabria, Santander, Spain (2017) [CERN-THESIS-2017-212].
- [13] L. Diehl, *Investigation of silicon sensors suitable for collider experiments in high luminosity environments*, Ph.D. Thesis, Albert Ludwig University of Freiburg, Freiburg im Breisgau, Germany (2022) [DOI:10.6094/UNIFR/228617].
- [14] O. Kałuzińska, *Silicon sensor characterisation and radiation hardness qualification for the CMS Endcap Calorimeter Upgrade for the High-Luminosity LHC*, MSc Thesis, Wrocław University of Science and Technology, Wrocław, Poland (2022) [CERN-THESIS-2022-086].
- [15] Cividec Instrumentation, *C2-HV broadband amplifier, 2 GHz, 40 dB*, <https://cividec.at/electronics-C2-HV.html>.
- [16] Particulars, *Advanced Measurement Systems, amplifiers*, <https://particulars.si/products.php?prod=amplifiers.html>.
- [17] OZ Optics, *Fused Couplers — Fiber Optic*, https://www.ozoptics.com/ALLNEW_PDF/DTS0033.pdf.
- [18] D. Campbell, A. Chilingarov and T. Sloan, *Frequency and temperature dependence of the depletion voltage from CV measurements for irradiated Si detectors*, *Nucl. Instrum. Meth. A* **492** (2002) 402.
- [19] C. Schinke et al., *Uncertainty analysis for the coefficient of band-to-band absorption of crystalline silicon*, *AIP Adv.* **5** (2015) 067168.
- [20] M.A. Green, *Self-consistent optical parameters of intrinsic silicon at 300 K including temperature coefficients*, *Sol. Energy Mater. Sol. Cells* **92** (2008) 1305.
- [21] K.G. Svantesson and N.G. Nilsson, *Determination of the temperature dependence of the free carrier and interband absorption in silicon at 1.06 μm* , *J. Phys. C* **12** (1979) 3837.
- [22] M.C. Muehlnikel, *Bulk radiation hardness and annealing studies of silicon sensors for the CMS endcap calorimeter upgrade*, Master's Thesis, Heidelberg University, Heidelberg, Germany (2024) [CERN-THESIS-2024-280].
- [23] G. Kramberger et al., *Determination of effective trapping times for electrons and holes in irradiated silicon*, *Nucl. Instrum. Meth. A* **476** (2002) 645.

- [24] G. Kramberger, V. Cindro, I. Mandic and M. Mikuz, *Impact of annealing of trapping times on charge collection in irradiated silicon detectors*, *Nucl. Instrum. Meth. A* **579** (2007) 762.
- [25] W. Adam et al., *Trapping in irradiated p-on-n silicon sensors at fluences anticipated at the HL-LHC outer tracker*, *2016 JINST* **11** P04023 [[arXiv:1505.01824](#)].
- [26] N. Akchurin et al., *Charge collection and electrical characterization of neutron irradiated silicon pad detectors for the CMS High Granularity Calorimeter*, *2020 JINST* **15** P09031 [[arXiv:2005.08051](#)].
- [27] L. Diehl et al., *Annealing studies on irradiated p-type silicon strip sensors designed for the ATLAS phase II tracking detector*, *Rad. Applic.* **3** (2018) 117.
- [28] W. Adam et al., *Experimental Study of Different Silicon Sensor Options for the Upgrade of the CMS Outer Tracker*, *2020 JINST* **15** P04017 [[arXiv:2001.02023](#)].
- [29] G. Casse et al., *Annealing at Different Temperatures of Silicon Microstrip Detectors After Severe Hadron Irradiation*, *IEEE Trans. Nucl. Sci.* **59** (2012) 419.
- [30] F. Bouyjou et al., *HGCROC3: the front-end readout ASIC for the CMS High Granularity Calorimeter*, *2022 JINST* **17** C03015.